

Software-based Microarchitectural Attacks

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Frame Rate of Slide Deck: 11 fps













1337 4242

FOOD CACHE

Revolutionary concept!

Store your food at home,
never go to the grocery store
during cooking.

Can store **ALL** kinds of food.

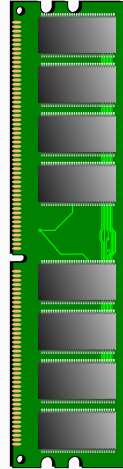
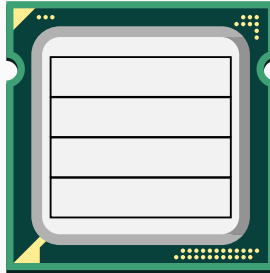
ONLY TODAY INSTEAD OF ~~\$1,300~~

\$1,299

ORDER VIA PHONE: +555 12345

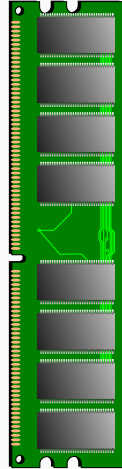
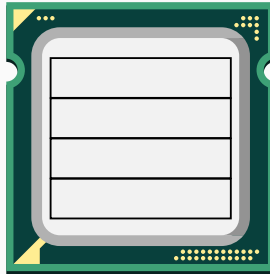



```
printf("%d", i);  
printf("%d", i);
```



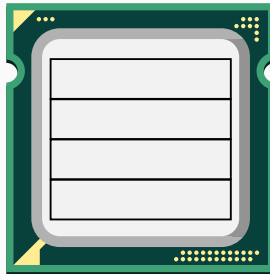
```
printf("%d", i);  
printf("%d", i);
```

Cache miss

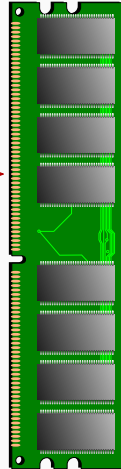


```
printf("%d", i);  
printf("%d", i);
```

Cache miss

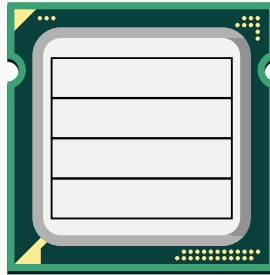


Request



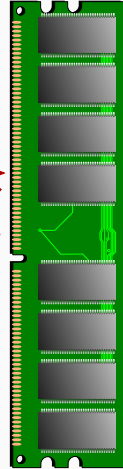
```
printf("%d", i);  
printf("%d", i);
```

Cache miss



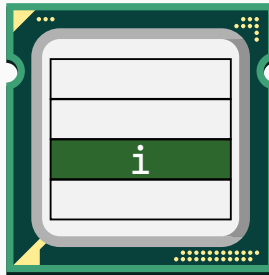
Request

Response



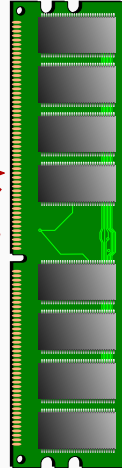
```
printf("%d", i);  
printf("%d", i);
```

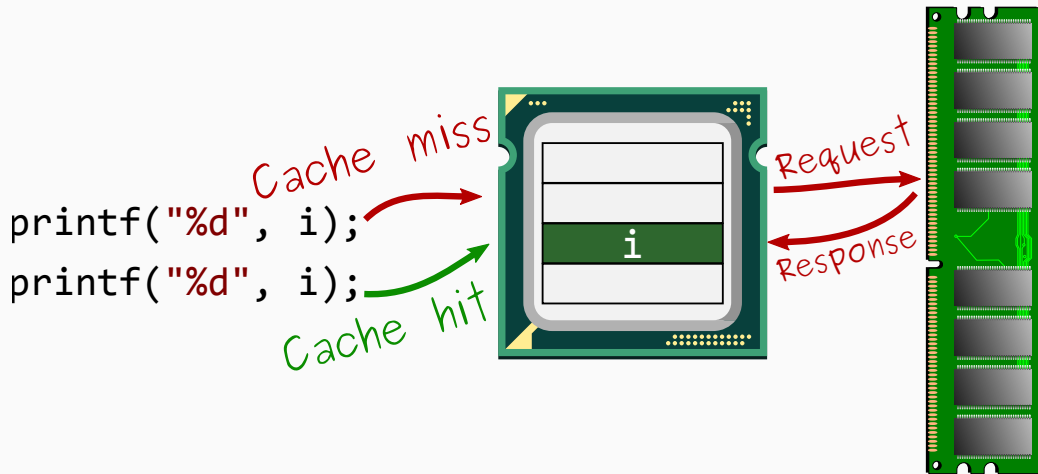
Cache miss



Request

Response



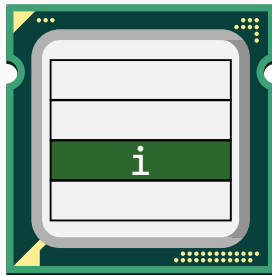


DRAM access,
slow

```
printf("%d", i);  
printf("%d", i);
```

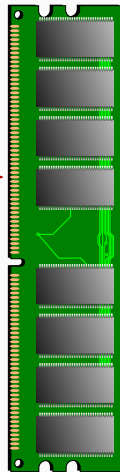
Cache miss

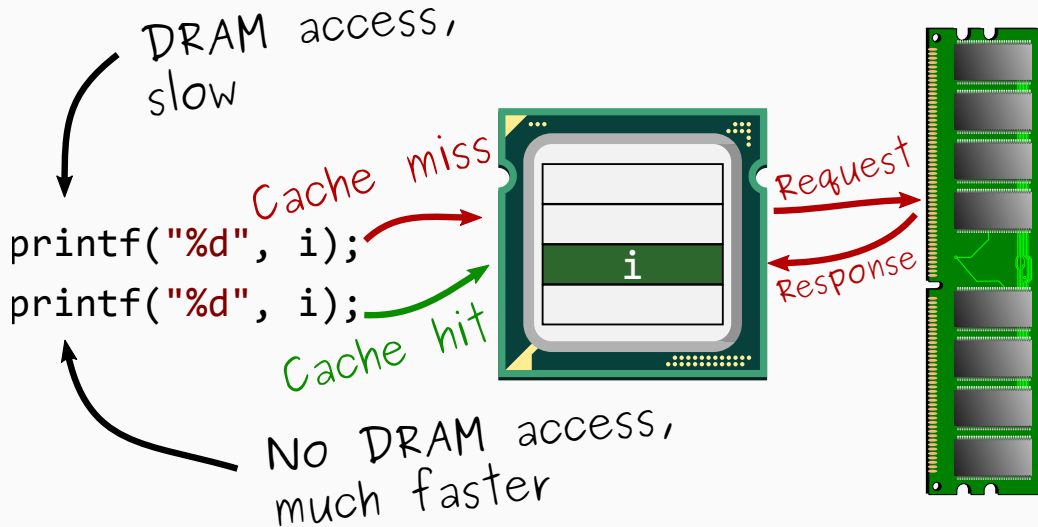
Cache hit



Request

Response

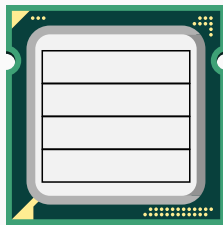




Shared Memory

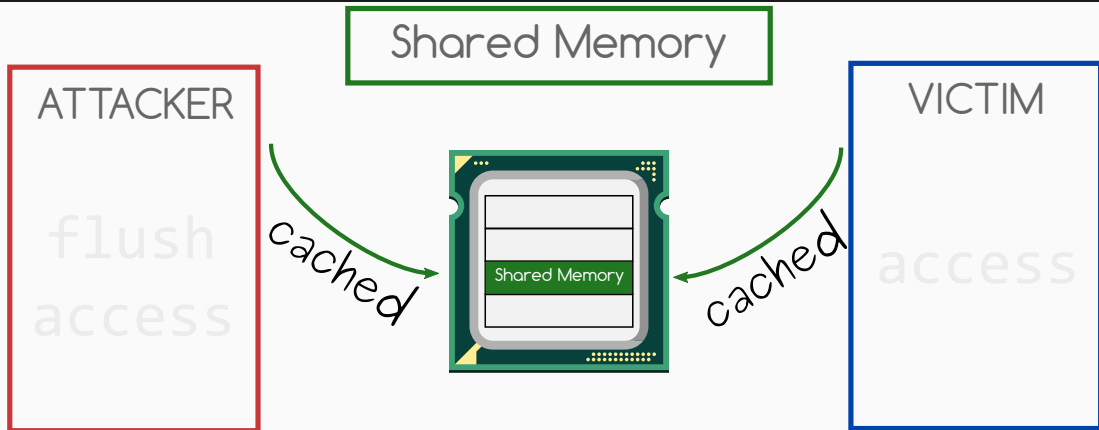
ATTACKER

flush
access



VICTIM

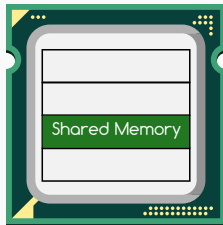
access



Shared Memory

ATTACKER

flush
access



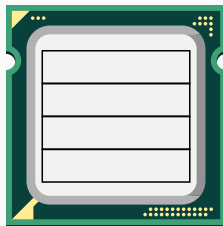
VICTIM

access

Shared Memory

ATTACKER

flush
access



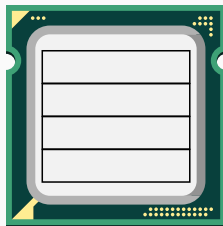
VICTIM

access

Shared Memory

ATTACKER

flush
access



VICTIM

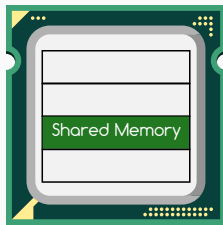
access



Shared Memory

ATTACKER

flush
access



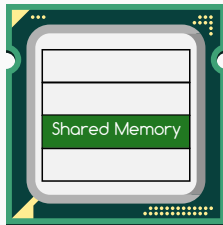
VICTIM

access

Shared Memory

ATTACKER

flush
access



VICTIM

access

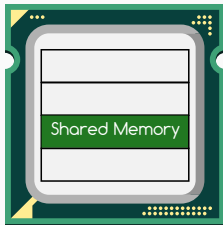
Shared Memory

ATTACKER

VICTIM

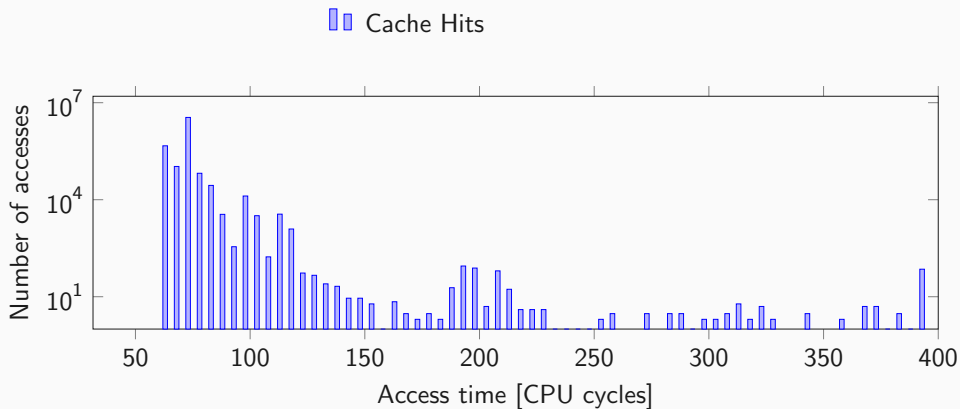
flush

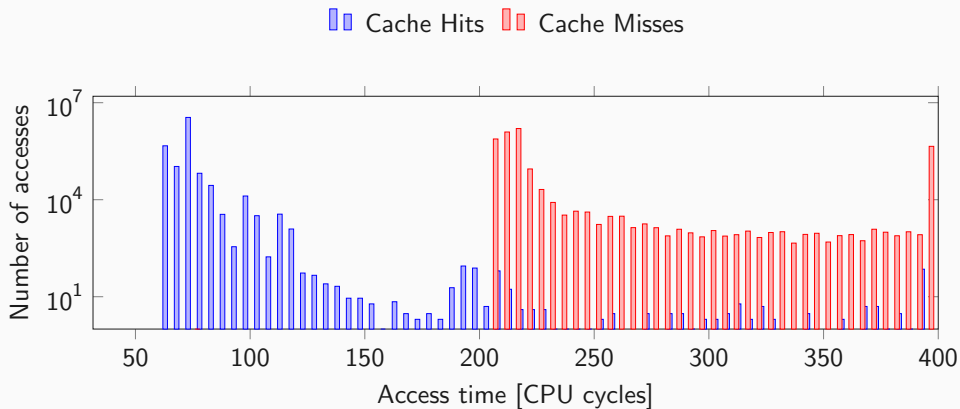
access



access

fast if victim accessed data,
slow otherwise

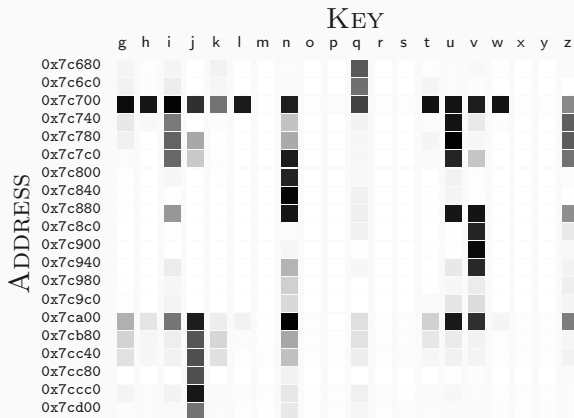




```
Terminal
File Edit View Search Terminal Help
% sleep 2; ./spy 300 7f05140a4000-7f051417b000 r-xp 0x20000 08:02 26
8050 /usr/lib/x86_64-linux-gnu/gedit/libgedit.so
```

```
Terminal
File Edit View Search Terminal Help
shark% ./spy
```

```
Untitled Document 1
Open + Save - + x
1
I
Plain Text Tab Width: 2 Ln 1, Col 1 INS
```





```
File Edit View Bookmarks Settings Help
local -- Konsole
ssh -l root 172.31.31.32 -p 22
```

```
File Edit View Bookmarks Settings Help
root@ip-172-31-31-32 ~ %
```

```
File Edit View Bookmarks Settings Help
-- alert 0.4.5 on ip-172-31-31-32 --
[...]
```

Active Interface: eth0		Interface Speed: unknown	
Current RX Speed:	0.01 KB/s	Current TX Speed:	0.33 KB/s
Graph Top RX Speed:	0.98 KB/s	Graph Top TX Speed:	2.64 KB/s
Overall Top RX Speed:	0.98 KB/s	Overall Top TX Speed:	2.64 KB/s
Received Packets:	64	Transmitted Packets:	61
Bytes Received:	0.045 MB	Bytes Transmitted:	0.039 MB
Errors on Receiving:	0	Errors on Transmission:	0

```
File Edit View Bookmarks Settings Help
root@ip-172-31-31-32 ~ %
```

```
File Edit View Bookmarks Settings Help
-- alert 0.4.5 on ip-172-31-31-32 --
[...]
```

Active Interface: eth0		Interface Speed: unknown	
Current RX Speed:	0.05 KB/s	Current TX Speed:	0.29 KB/s
Graph Top RX Speed:	0.36 KB/s	Graph Top TX Speed:	0.84 KB/s
Overall Top RX Speed:	0.36 KB/s	Overall Top TX Speed:	0.84 KB/s
Received Packets:	36	Transmitted Packets:	26
Bytes Received:	0.003 MB	Bytes Transmitted:	0.010 MB
Errors on Receiving:	0	Errors on Transmission:	0

HELLO FROM THE OTHER SIDE (DEMO):
VIDEO STREAMING OVER CACHE COVERT CHANNEL



Back to Work

*6. Cook everything until
vegetables are soft*

*6. Cook everything until
vegetables are soft*

*7. Serve with cooked
and peeled potatoes*





Wait for an hour





Wait for an hour

LATENCY

1. Wash and cut
vegetables

2. Pick the basil leaves
and set aside

3. Heat 2 tablespoons of
oil in a pan

4. Fry vegetables until
golden and softened



Dependency

1. Wash and cut vegetables

2. Pick the basil leaves and set aside

3. Heat 2 tablespoons of oil in a pan

4. Fry vegetables until golden and softened

Parallelize



```
int width = 10, height = 5;

float diagonal = sqrt(width * width
                      + height * height);
int area = width * height;

printf("Area %d x %d = %d\n", width, height, area);
```

Dependency



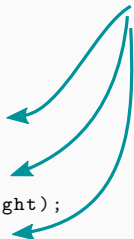
```
int width = 10, height = 5;
```

```
float diagonal = sqrt(width * width  
                      + height * height);
```

```
int area = width * height;
```

```
printf("Area %d x %d = %d\n", width, height, area);
```

Parallelize



```
char data = *(char*)0xffffffff81a000e0;  
printf("%c\n", data);
```





```
char data = *(char*)0xffffffff81a000e0;  
printf("%c\n", data);
```

```
segfault at ffffffff81a000e0 ip  
0000000000400535  
sp 00007ffce4a80610 error 5 in reader
```

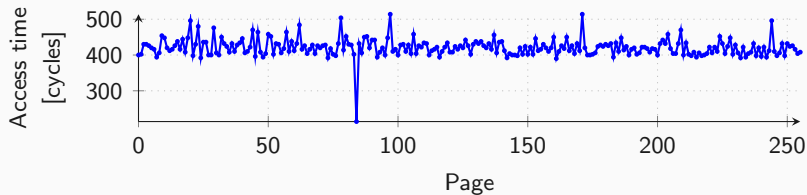



Adapted code

```
*(volatile char*)0;  
array[84 * 4096] = 0; // unreachable
```



Flush+Reload over all pages of the array





Flush+Reload over all pages of the array



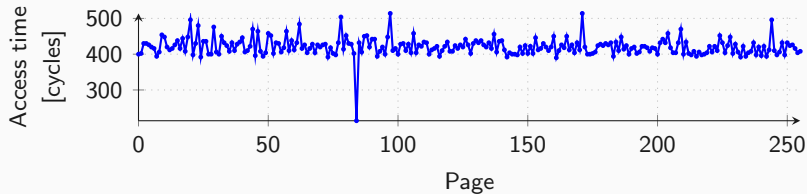
This also works on AMD and ARM!



- Combine the two things

```
char data = *(char*)0xffffffff81a000e0;  
array[data * 4096] = 0;
```

Flush+Reload again...



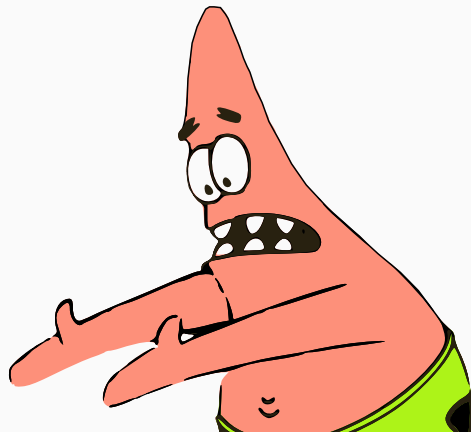
... Meltdown actually works.

attacker@meltdown ~/exploit %

victim@meltdown ~ %

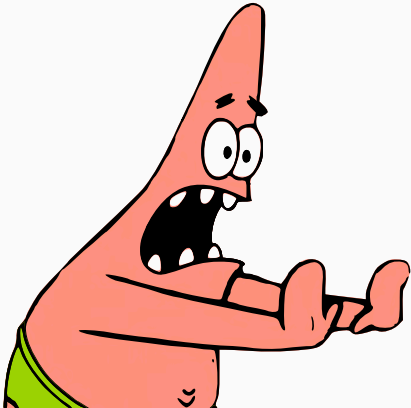
- Kernel addresses in user space are a problem (the wall does not work)

- Kernel addresses in user space are a problem (the wall does not work)
- Why don't we take the kernel addresses...





- ...and remove them if not needed?



- ...and remove them if not needed?
- User accessible check in hardware is not reliable





Kernel **A**ddress **I**solation to have **S**ide channels **E**fficiently **R**emoved

KAISER /'kAɪzə/

1. [german] Emperor, ruler of an empire
2. largest penguin, emperor penguin



Kernel **A**ddress **I**solation to have **S**ide channels **E**fficiently **R**emoved



Let us get rid of bottlenecks

Use the
naughty/nice list
of last year





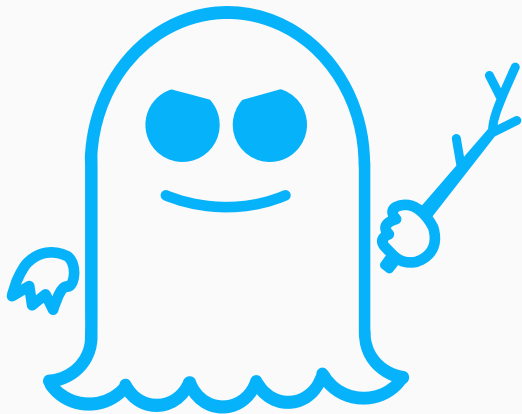
Finally, check
predictions with
list of this year

Throwing away
wrongly manufac-
tured presents





Correct
predictions
result in
free time



SPECTRE



```
index = 0;
```

```
char* data = "textKEY";
```

```
if (index < 4)
```

then

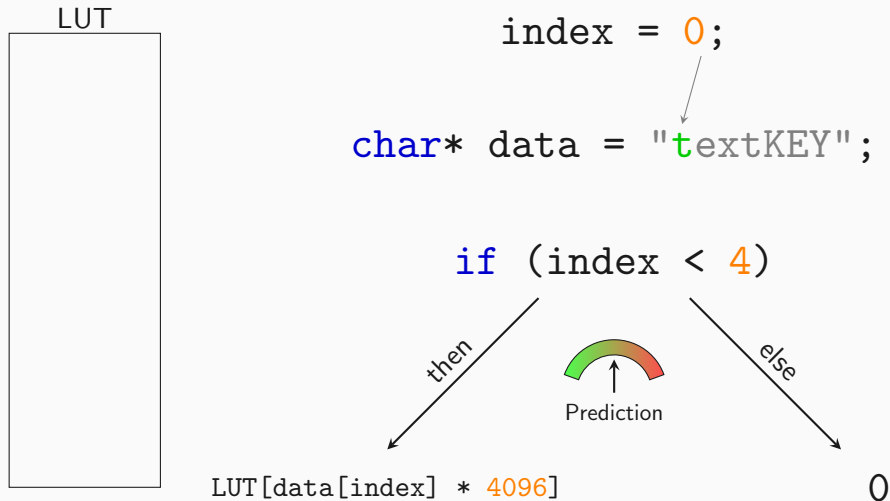


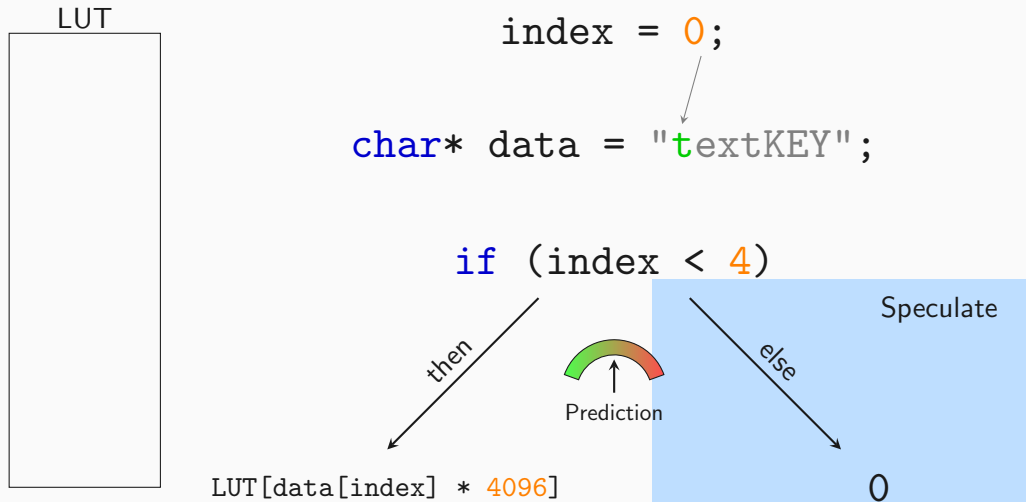
Prediction

else

```
LUT[data[index] * 4096]
```

```
0
```







index = 0;

char* data = "ttextKEY";

if (index < 4)

Execute

then



LUT[data[index] * 4096]

else

0



```
index = 1;
```

```
char* data = "textKEY";
```

```
if (index < 4)
```

then

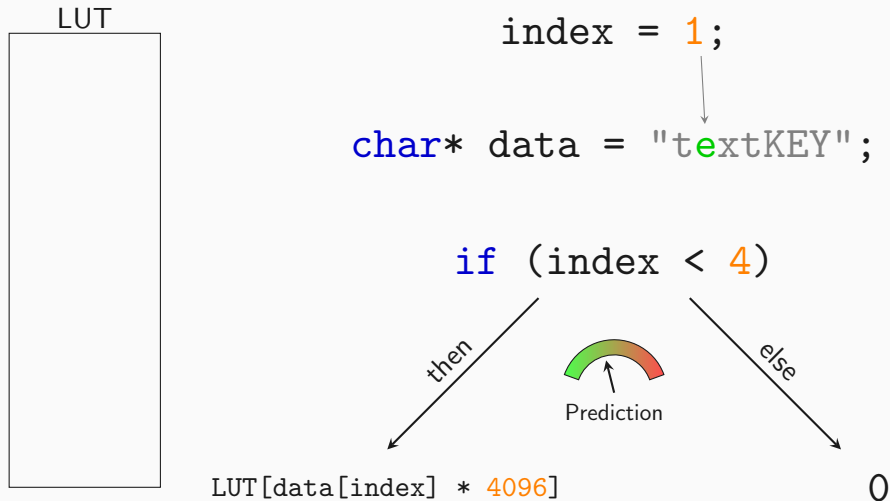


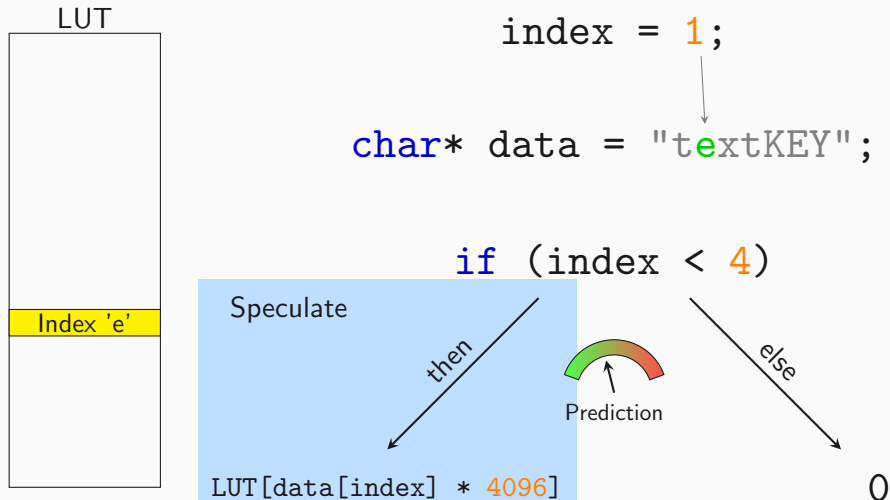
Prediction

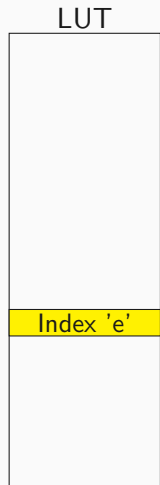
else

```
LUT[data[index] * 4096]
```

```
0
```





```
index = 1;
```

```
char* data = "textKEY";
```

```
if (index < 4)
```

then



else

```
LUT[data[index] * 4096]
```

```
0
```



```
index = 2;
```

```
char* data = "textKEY";
```

```
if (index < 4)
```

then

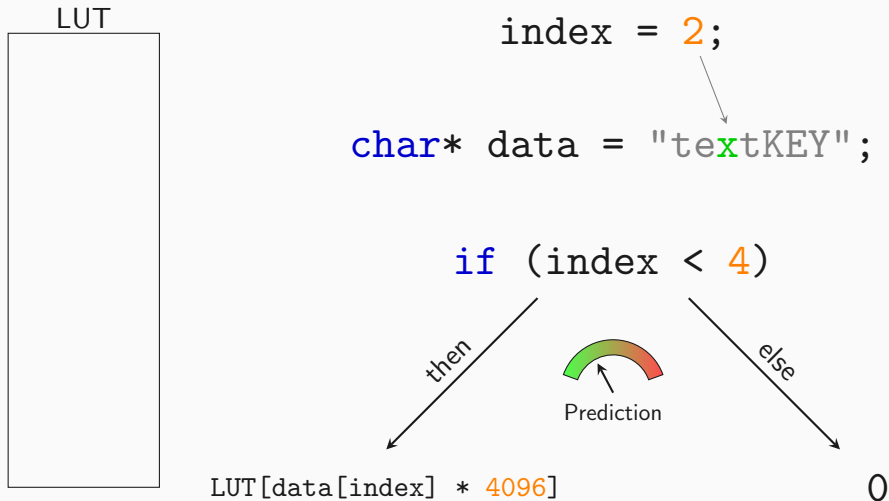


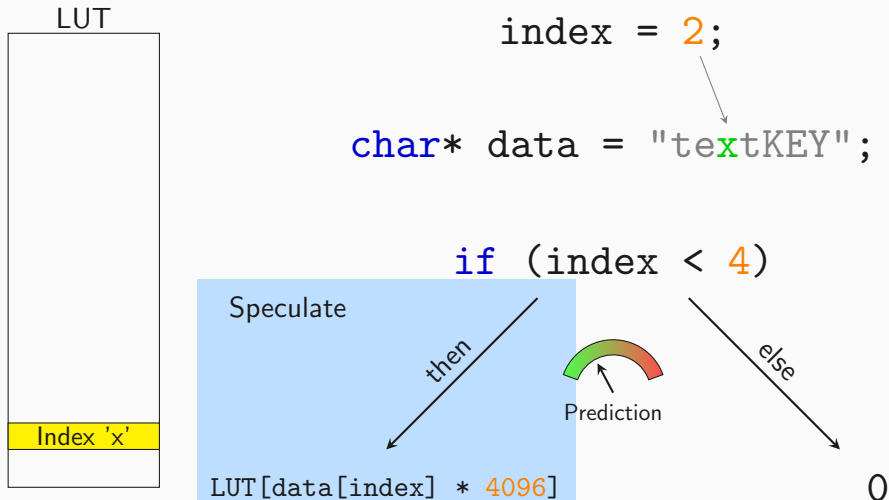
Prediction

else

```
LUT[data[index] * 4096]
```

```
0
```







index = 2;

char* data = "textKEY";

if (index < 4)

then



else

LUT[data[index] * 4096]

0



```
index = 3;
```

```
char* data = "textKEY";
```

```
if (index < 4)
```

then

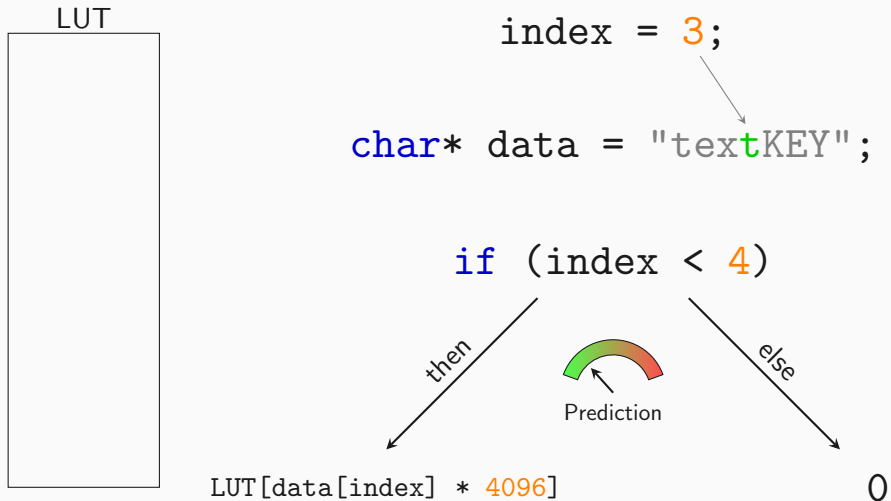


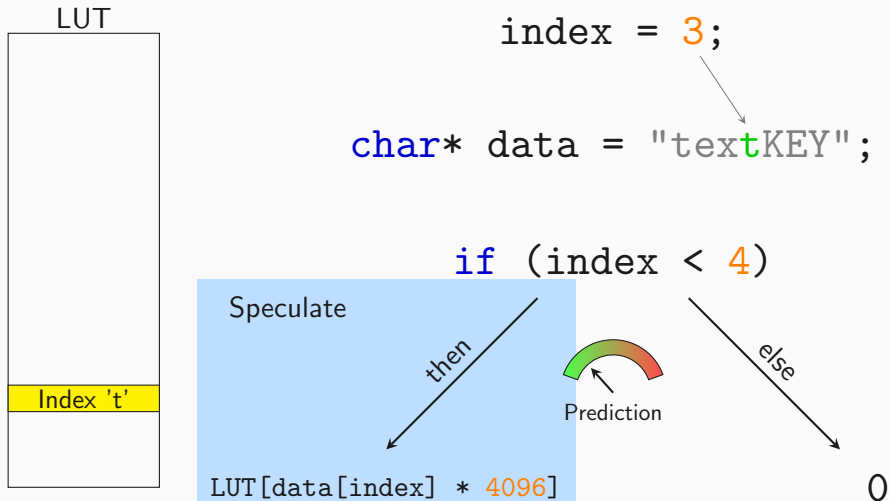
Prediction

else

```
LUT[data[index] * 4096]
```

```
0
```





index = 3;

char* data = "textKEY";

if (index < 4)

then



else

LUT[data[index] * 4096]

0



```
index = 4;
```

```
char* data = "textKEY";
```

```
if (index < 4)
```

then

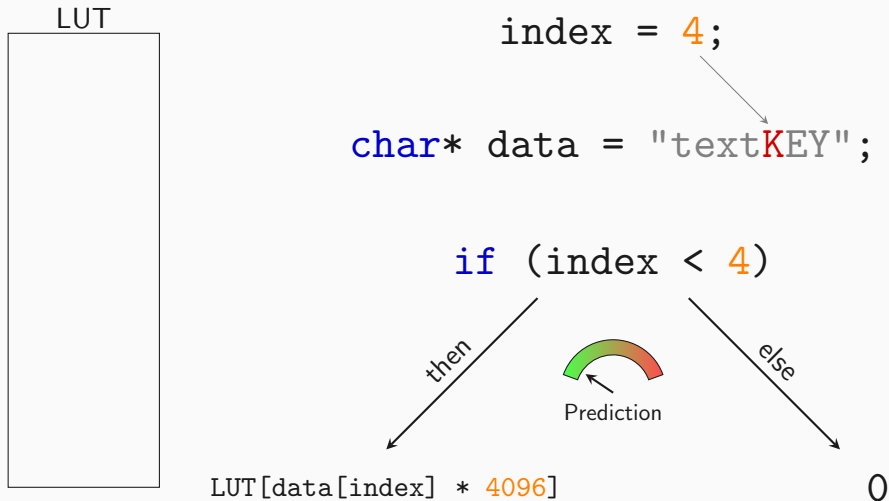


Prediction

else

```
LUT[data[index] * 4096]
```

```
0
```





index = 4;

char* data = "textKEY";

if (index < 4)

Speculate

then

LUT[data[index] * 4096]



else

0



index = 4;

char* data = "textKEY";

if (index < 4)

then

LUT[data[index] * 4096]



else

Execute

0



```
index = 5;
```

```
char* data = "textKEY";
```

```
if (index < 4)
```

then

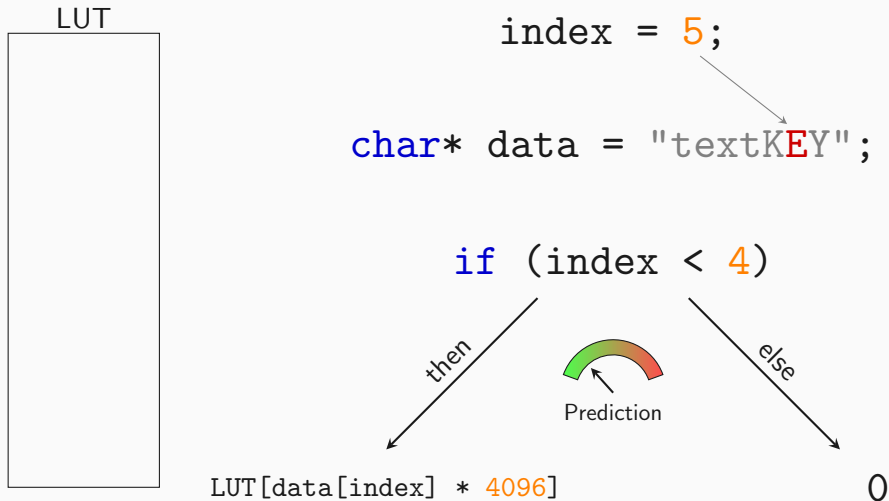


Prediction

else

LUT[data[index] * 4096]

0





index = 5;

char* data = "textKEY";

if (index < 4)

Speculate

then

LUT[data[index] * 4096]



else

0



```
index = 5;
```

```
char* data = "textKEY";
```

```
if (index < 4)
```

then

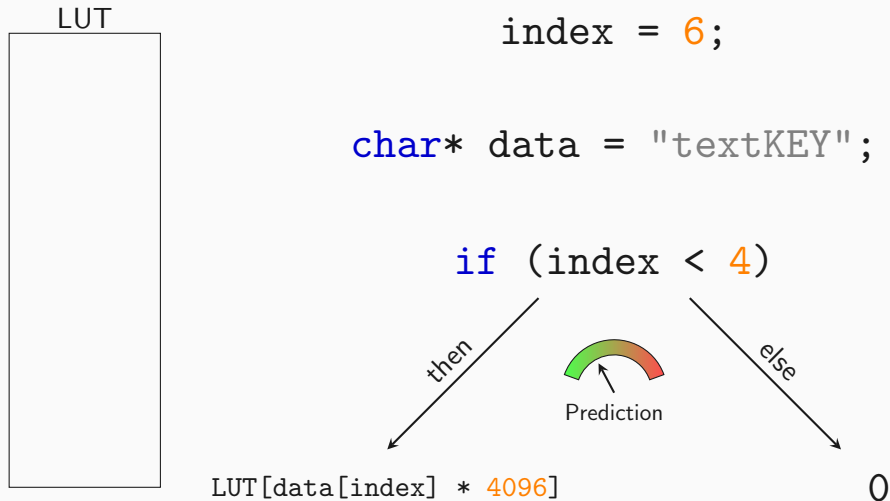
```
LUT[data[index] * 4096]
```

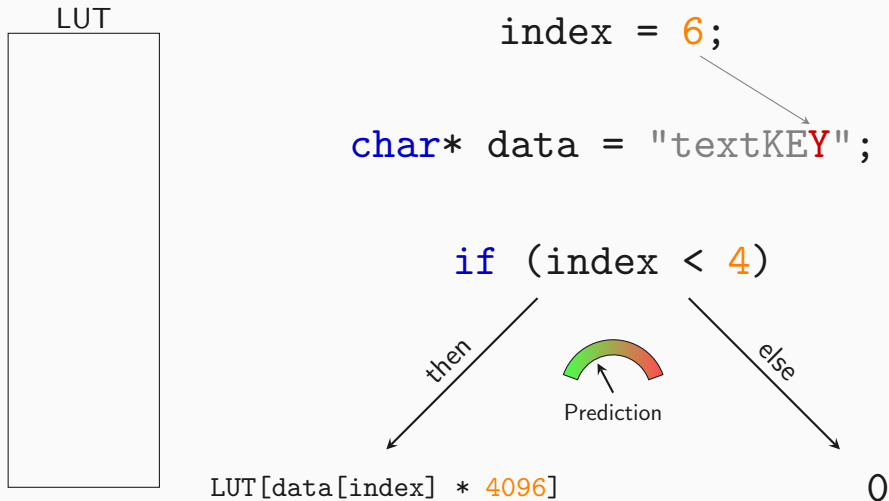


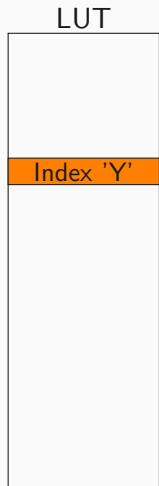
else

Execute

0







```
index = 6;
```

```
char* data = "textKEY";
```

```
if (index < 4)
```

Speculate

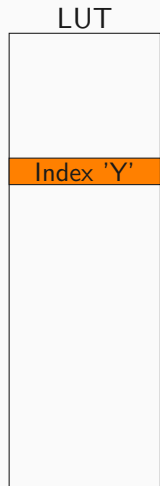
then

```
LUT[data[index] * 4096]
```



else

0



```
index = 6;
```

```
char* data = "textKEY";
```

```
if (index < 4)
```

then

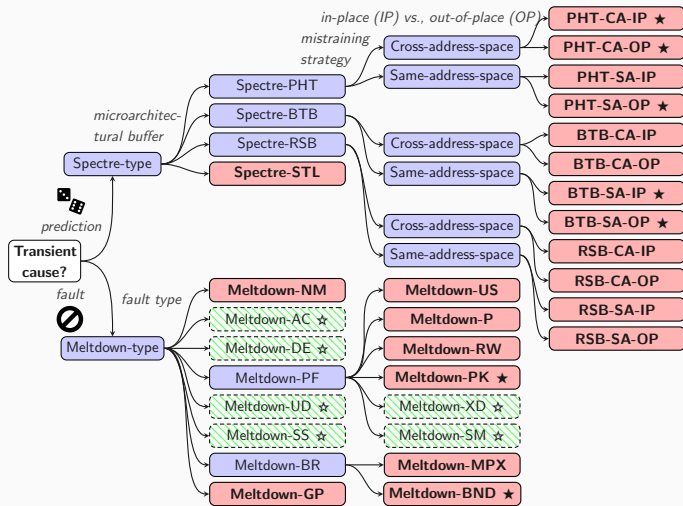
```
LUT[data[index] * 4096]
```



else

Execute

0





BLOCKCHAIN

HHD
HISTORY.COM

		Attack \ Defense	Defense																			
			InvisiSpec	SafeSpec	DAWG	RSB	Stuffing	Retpoline	Poison Value	Index Masking	Site Isolation	SLH	YSNB	IBRS	STIPB	IBPB	Serialization	Taint Tracking	Timer Reduction	Sloth	SSBD/SSBB	
Intel	Spectre-PHT																					
	Spectre-BTB																					
	Spectre-RSB																					
	Spectre-STL																					
ARM	Spectre-PHT																					
	Spectre-BTB																					
	Spectre-RSB																					
	Spectre-STL																					
AMD	Spectre-PHT																					
	Spectre-BTB																					
	Spectre-RSB																					
	Spectre-STL																					

Mitigated (●), partially mitigated (◐), not mitigated (○), theoretically mitigated (■), theoretically impeded (▣), not theoretically impeded (□), or out of scope (◇).



- new class of attacks



- new class of attacks
- many problems to solve around microarchitectural attacks and especially transient execution attacks



- new class of attacks
- many problems to solve around microarchitectural attacks and especially transient execution attacks
- dedicate more time into identifying problems and not solely in mitigating known problems

Software-based Microarchitectural Attacks

Daniel Gruss

January 23, 2019

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