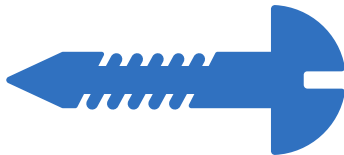


Flipping Bits from Software without Rowhammer

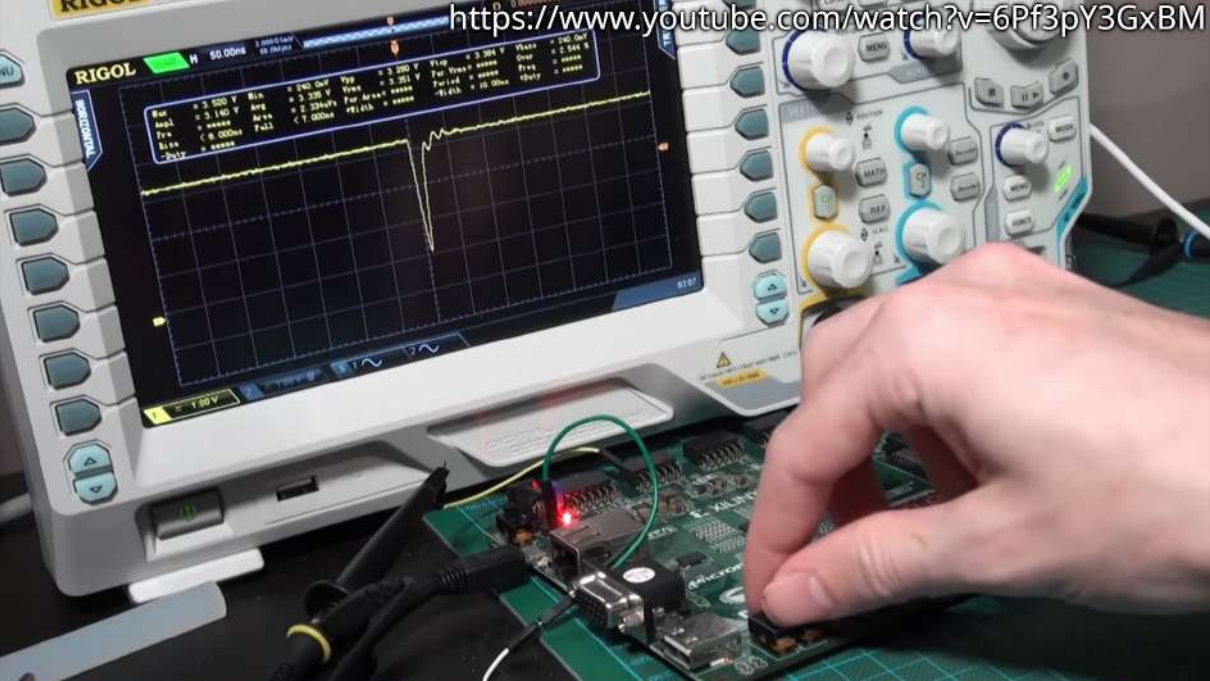
Kit Murdock, Daniel Gruss

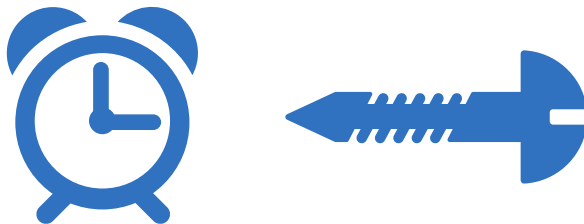
December 27, 2019

In the beginning...



A NEW CLASS OF FAULT ATTACKS





Adrian Tang et al. "CLKSCREW: Exposing the Perils of Security-Oblivious Energy Management". In: *USENIX Security Symposium*. 2017

DVFS

Changing the voltage and frequency of the CPU

Changing the voltage and frequency of the CPU

- Gamers want fast responses

Changing the voltage and frequency of the CPU

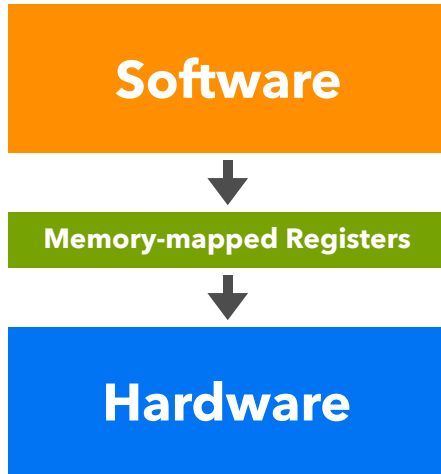
- Gamers want fast responses
- Cloud servers want high-assurance and low running costs

Changing the voltage and frequency of the CPU

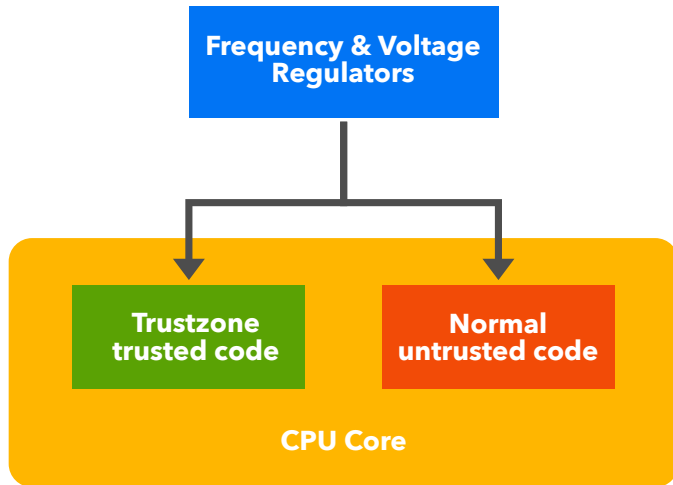
- Gamers want fast responses
- Cloud servers want high-assurance and low running costs
- What if the hardware gets hot?

Changing the voltage and frequency of the CPU

- Gamers want fast responses
- Cloud servers want high-assurance and low running costs
- What if the hardware gets hot?
- Optimal voltage & frequency is difficult!

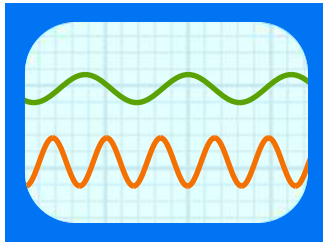


Memory Mapped Registers



CLKscrew attack

```
add.w    (a0)+,d1  
cmp.l    a0,d0  
bcc.s    loop  
movea.l  #$18E,a1  
cmp.w    (a1),d1  
bne.w    WrongChecksum
```



$$2 + 2 = 5$$



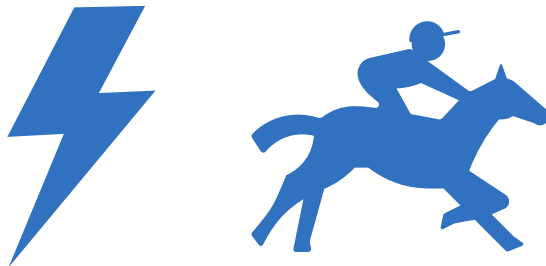


- Infer secret AES key that was stored within Trustzone



- Infer secret AES key that was stored within Trustzone
- Trick Trustzone into loading a self-signed app

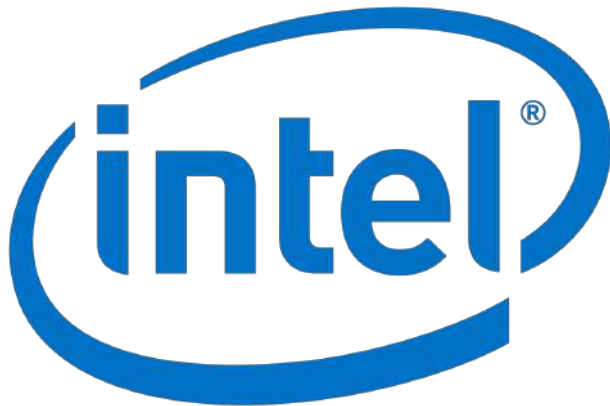




Pengfei Qiu et al. “VoltJockey: Breaching TrustZone by Software-Controlled Voltage Manipulation over Multi-core Frequencies”. In: *CCS*. 2019



**ARE WE ALL
THINKING THE
SAME THING?**







- RMClock



- RMClock
- Throttlestop



- RMClock
- Throttlestop
- linux-intel-undervolt



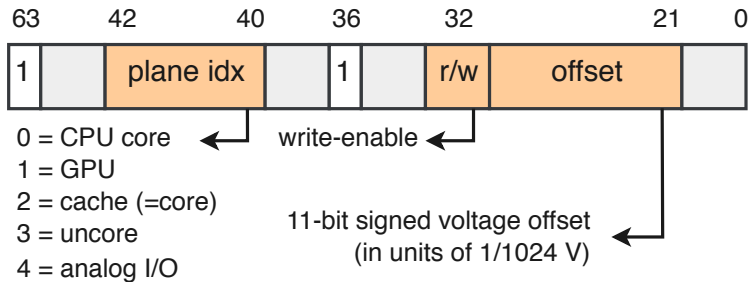
- RMClock
- Throttlestop
- linux-intel-undervolt
- Adrian Tang's PhD dissertation





msr 0x150

Intel Power Management



Will it fault?

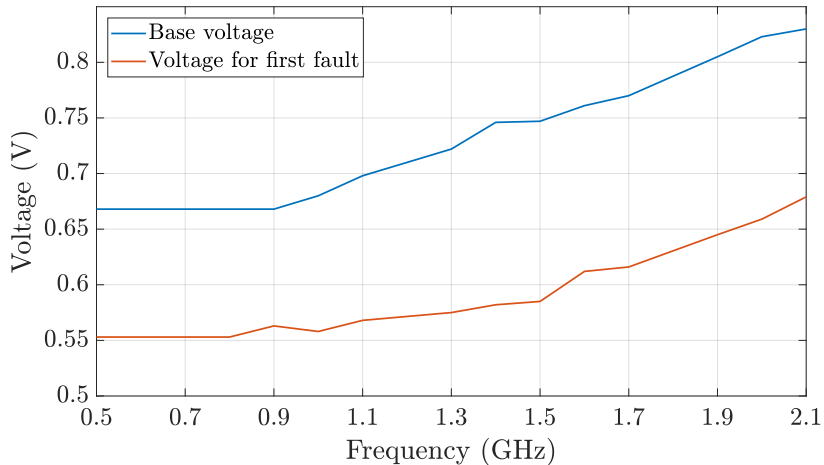
```
uint64_t multiplier = 0x1122334455667788;
uint64_t correct    = 0xdeadbeef * multiplier;
uint64_t var        = 0xdeadbeef * multiplier;

while (var == correct)
{
    var = 0xdeadbeef * multiplier;
}
uint64_t flipped_bits = var ^ correct;
```

bagger> █

I

Safe voltages





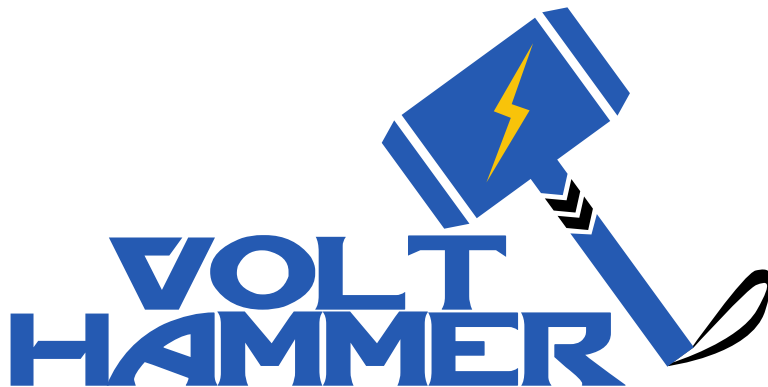
Aristotle Tzafalias

@Aristot73



thesis: given a cool name, researchers will be able to find a new vulnerability to match it, in a finite period of time; cool name-logo combination requirements have no measurable effect on aforementioned period.

We need a logo!

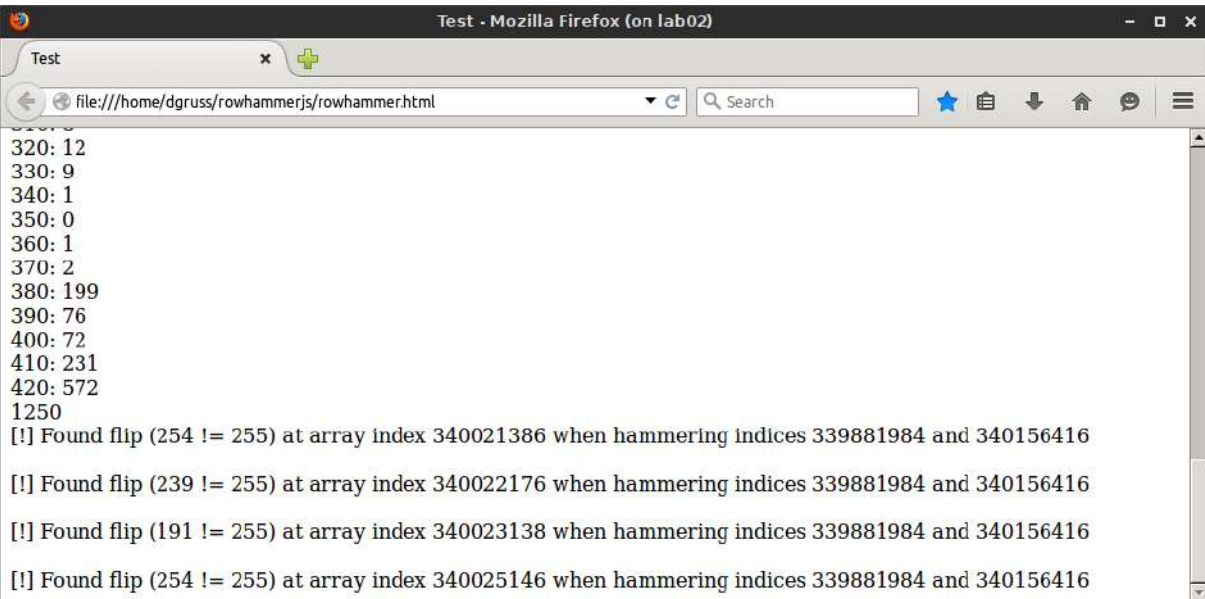




CTV

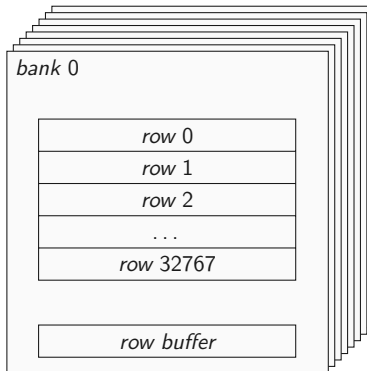


ROOT privileges for web apps!



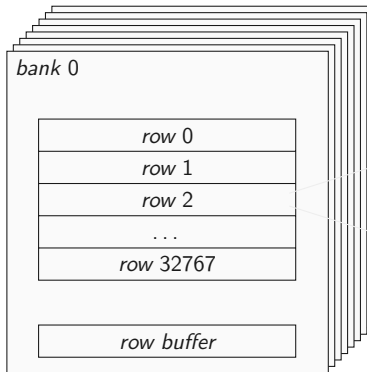
DRAM organization

chip

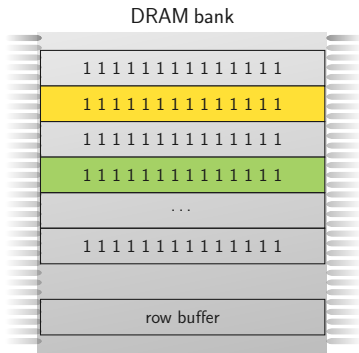


DRAM organization

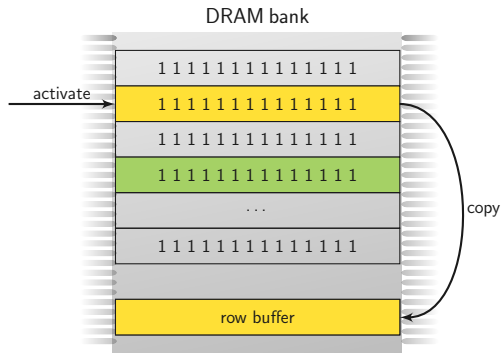
chip



64k cells
1 capacitor,
1 transistor each

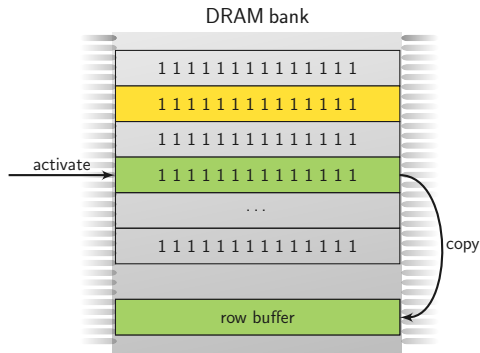


- Cells leak → need **refresh**
- Max. refresh interval to guarantee **data integrity**
- Cells leak faster upon proximate accesses → Rowhammer

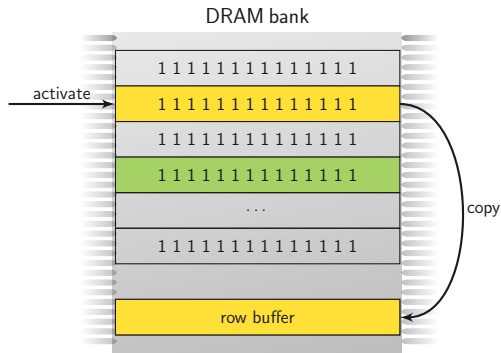


- Cells leak → need **refresh**
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Rowhammer

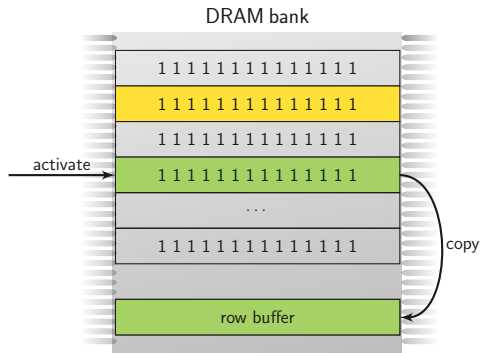


- Cells leak → need **refresh**
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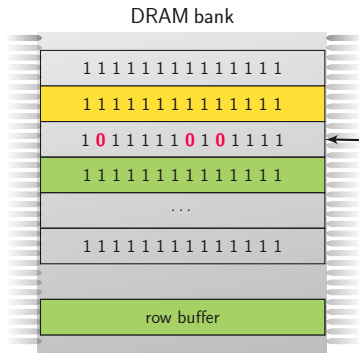


- Cells leak → need **refresh**
- Max. refresh interval to guarantee **data integrity**
- Cells leak faster upon proximate accesses → Rowhammer

Rowhammer

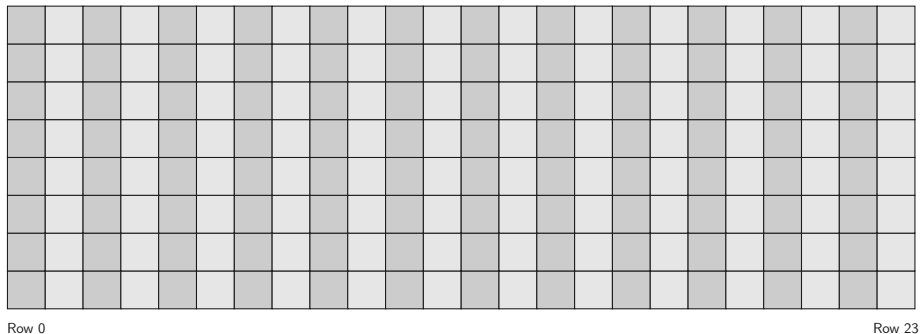


- Cells leak → need **refresh**
- Max. refresh interval to guarantee **data integrity**
- Cells leak faster upon proximate accesses → Rowhammer



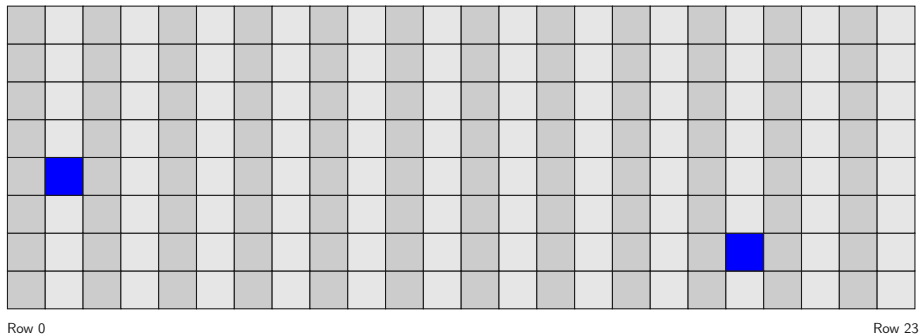
bit flips in row 2!

- Cells leak → need **refresh**
- Max. refresh interval to guarantee **data integrity**
- Cells leak faster upon proximate accesses → Rowhammer

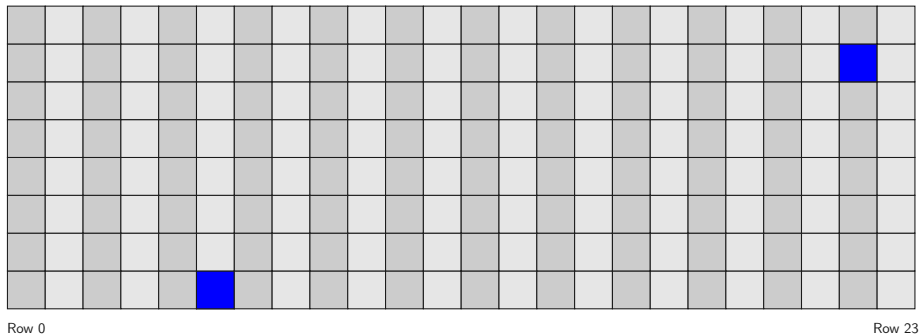


Hammering memory locations in different rows

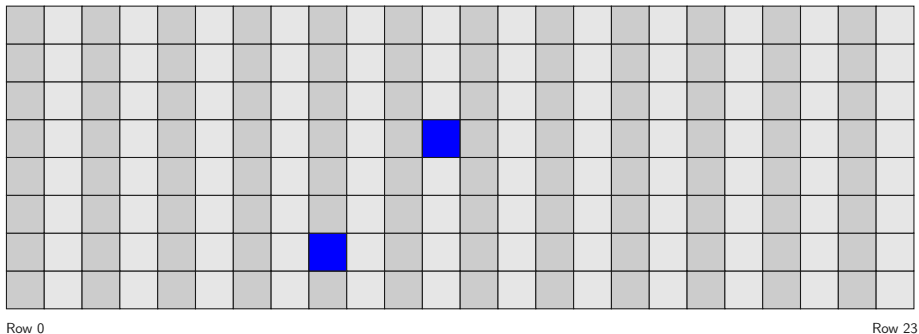
Search for page with flip



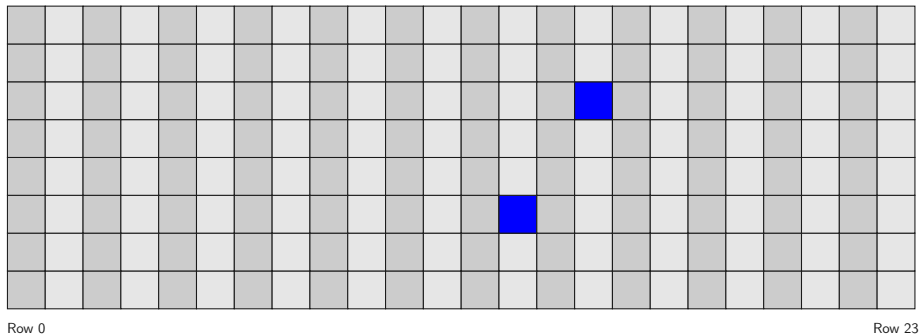
Hammering memory locations in different rows



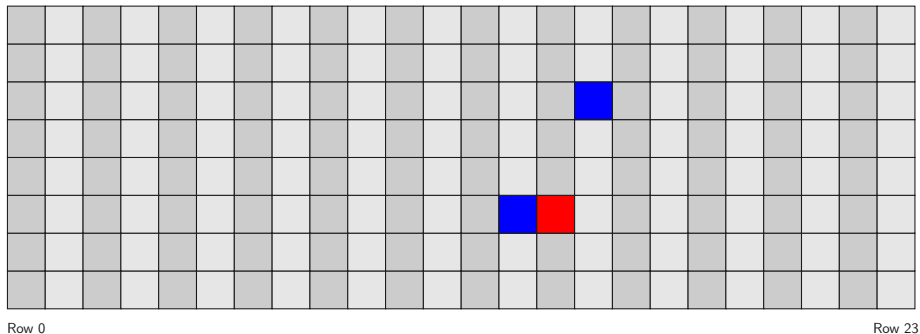
Hammering memory locations in different rows



Hammering memory locations in different rows

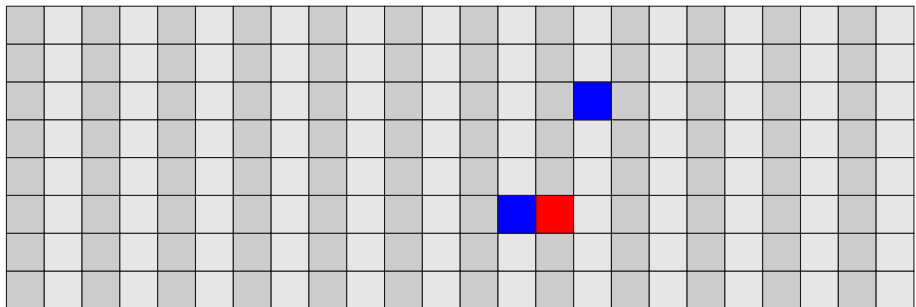


Hammering memory locations in different rows



Hammering memory locations in different rows

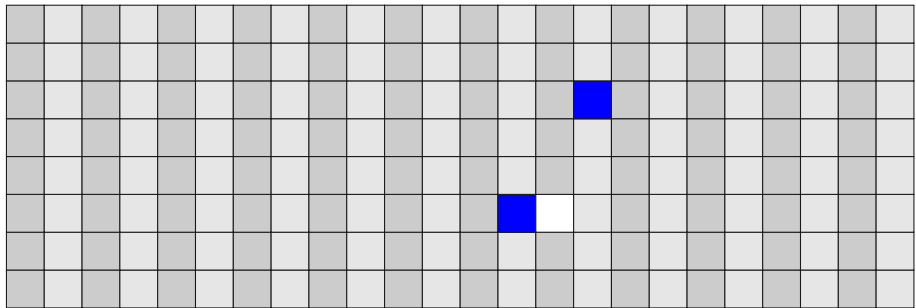
Release page with flip



Row 0

Row 23

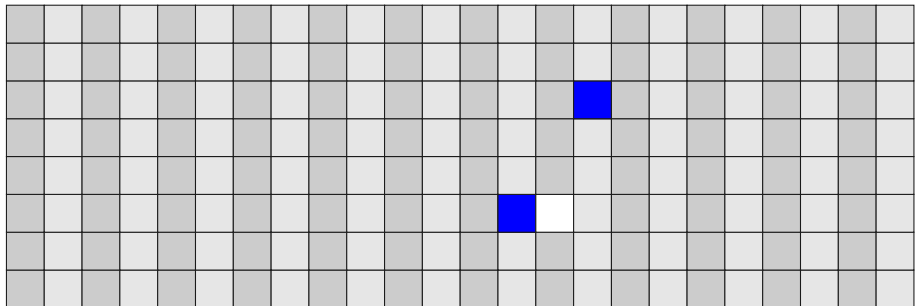
Release page with flip



Row 0

Row 23

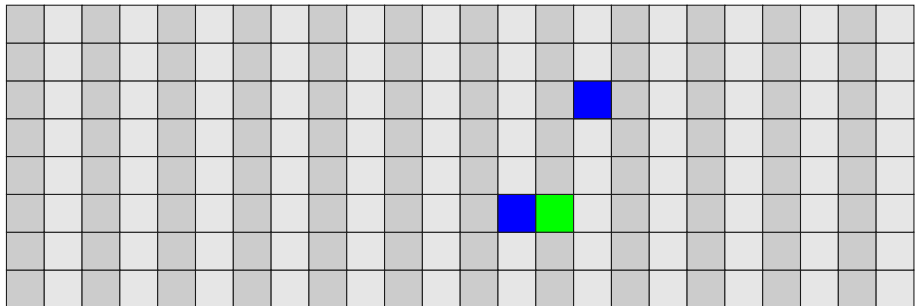
Fill all remaining memory with target pages



Row 0

Row 23

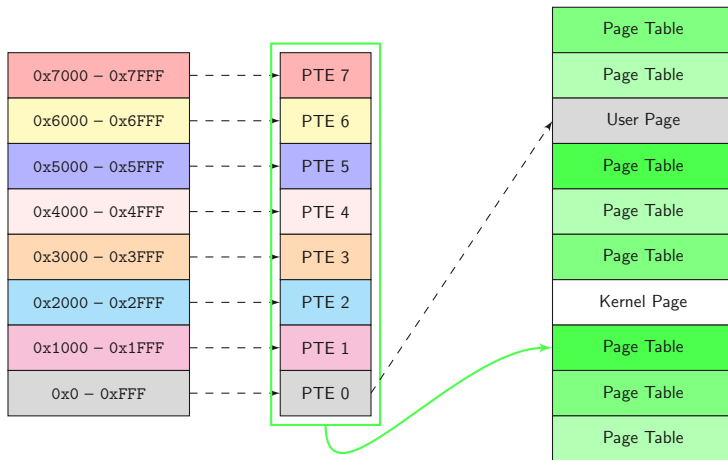
Fill all remaining memory with target pages



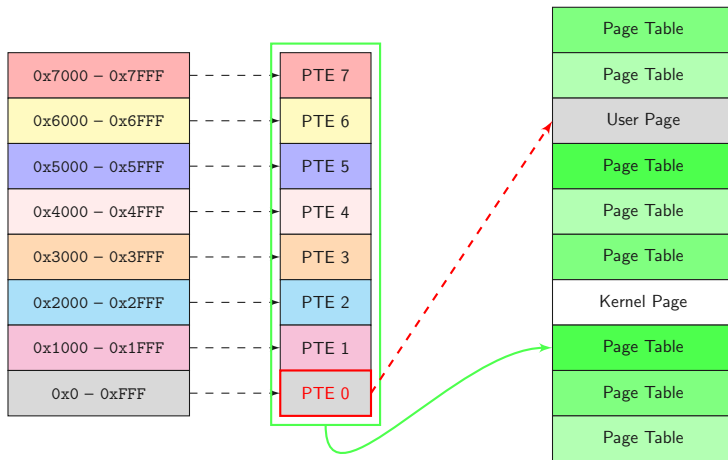
Row 0

Row 23

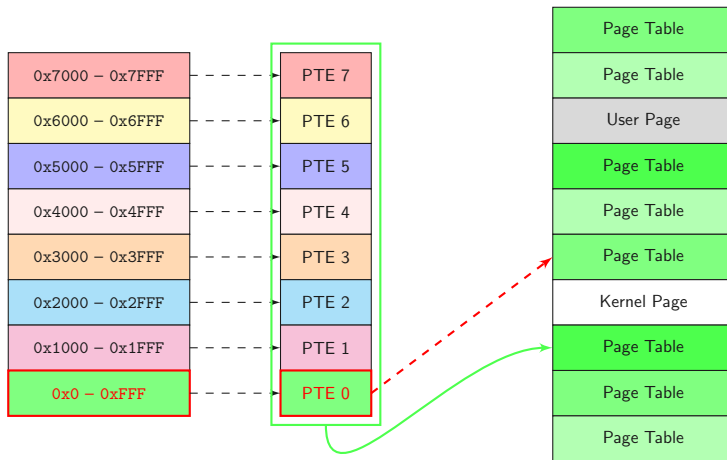
Page Table Example



Page Table Example



Page Table Example





















- DDR3 affected



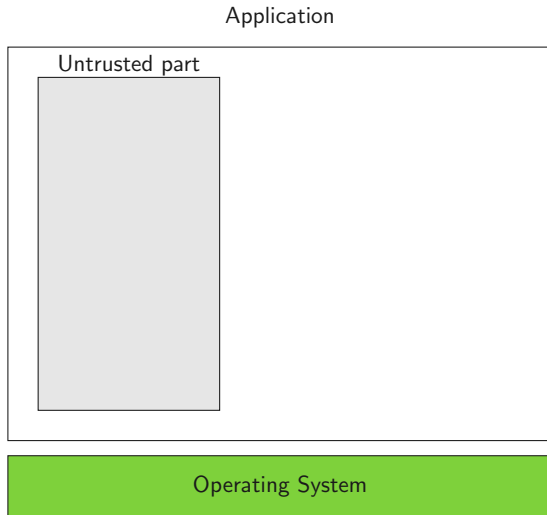
- DDR3 affected
- DDR4 affected

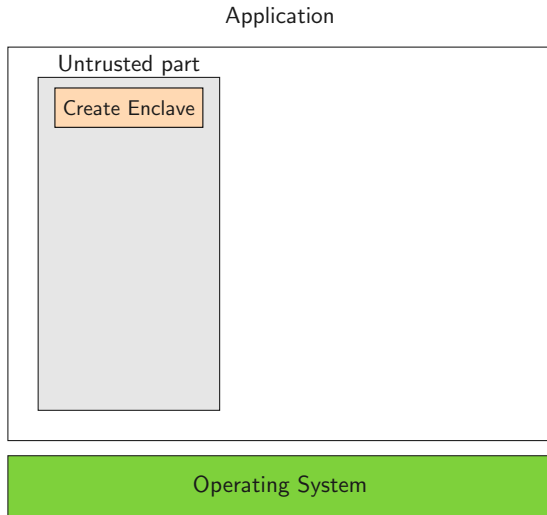


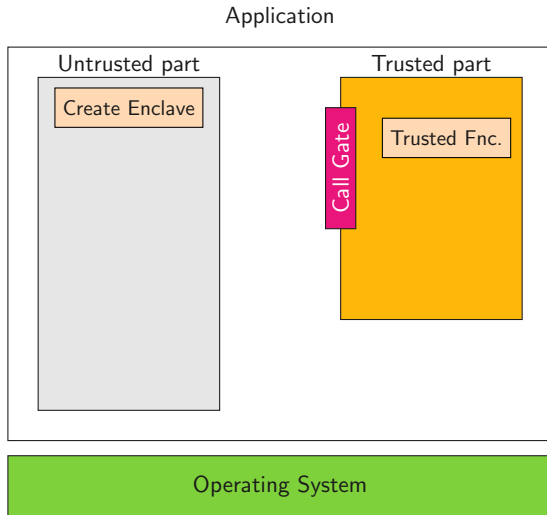
- DDR3 affected
- DDR4 affected
- Even ECC affected despite error correction!

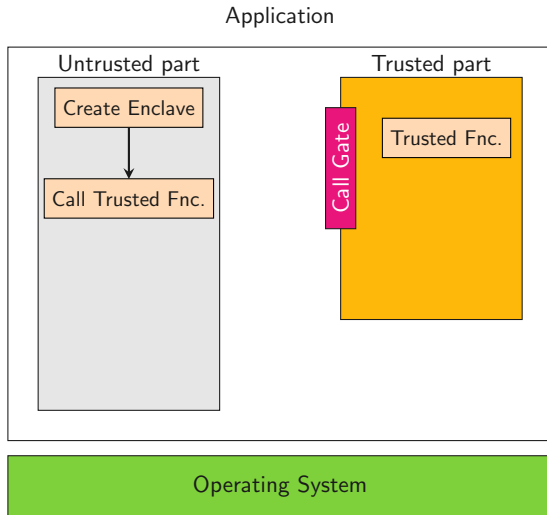


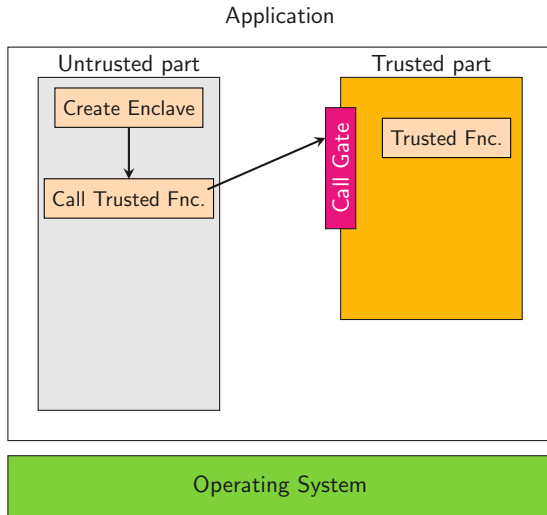
- DDR3 affected
 - DDR4 affected
 - Even ECC affected despite error correction!
- Can SGX's integrity protection prevent Rowhammer?

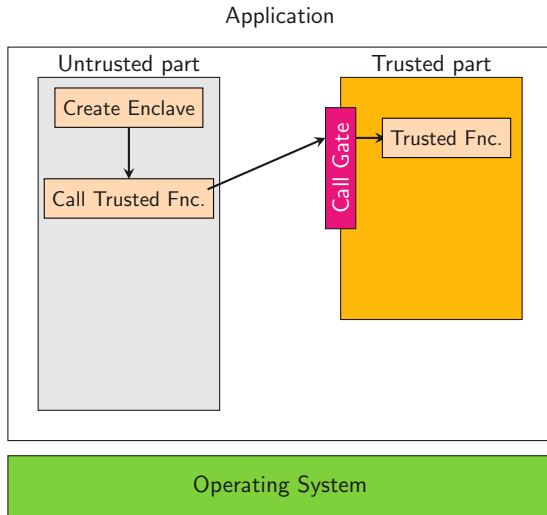


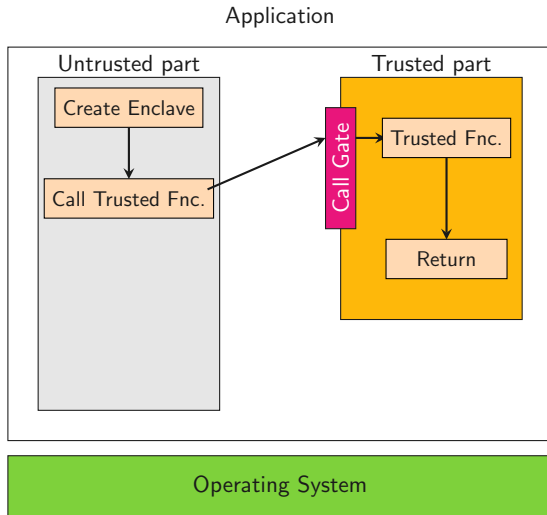


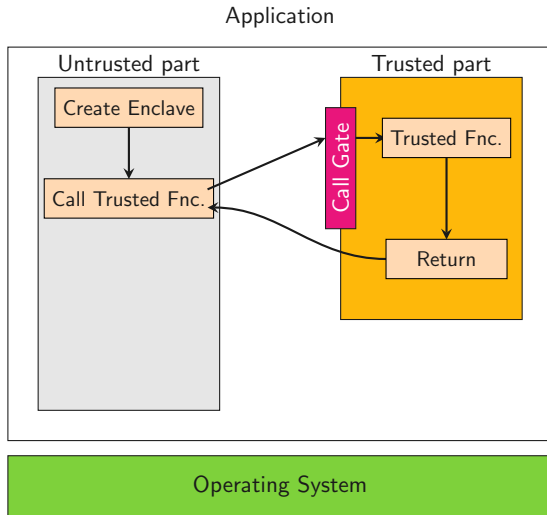


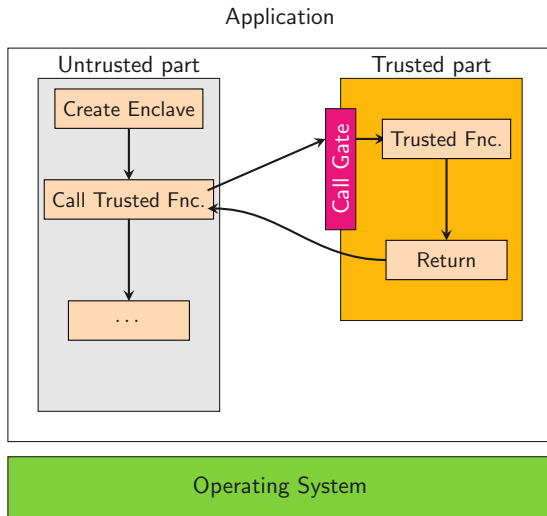


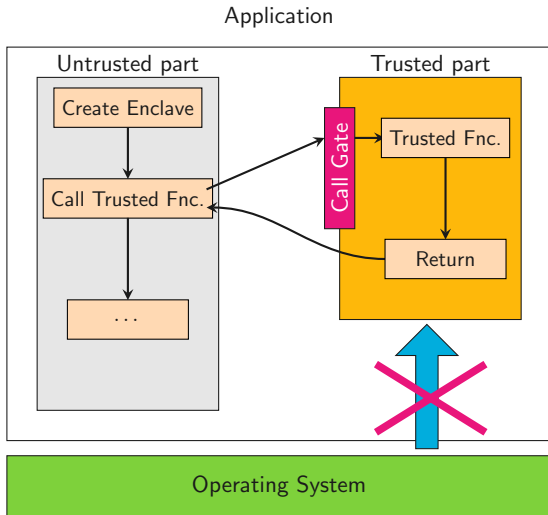








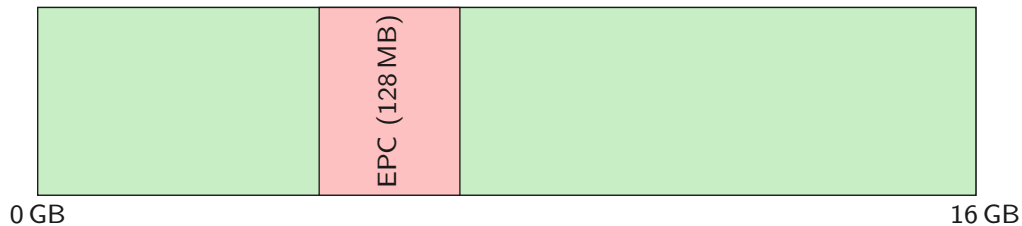


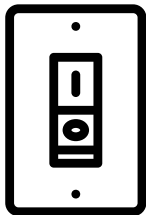


SGX Encrypted Memory

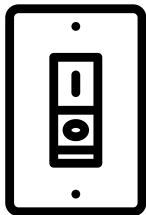


SGX Encrypted Memory

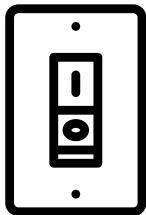




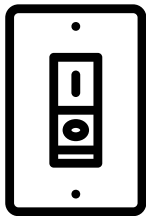
- Bit flips in the EPC?



- Bit flips in the EPC?
- Integrity check fails!



- Bit flips in the EPC?
 - Integrity check fails!
- Lock up memory controller



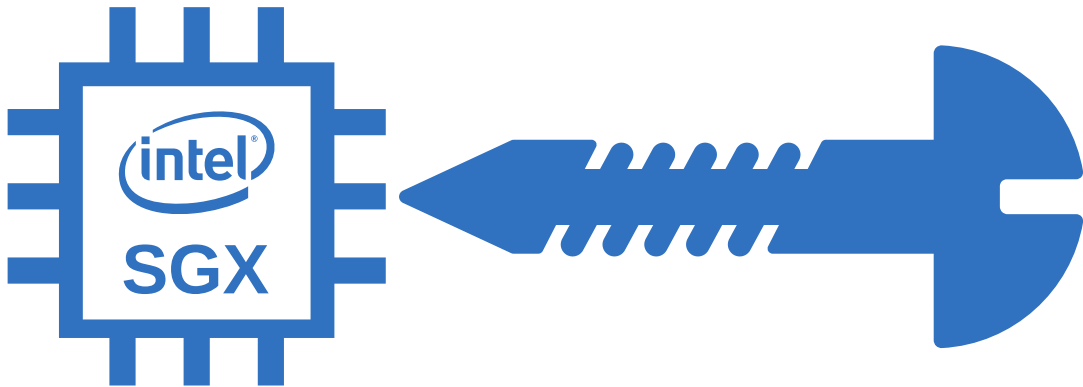
- Bit flips in the EPC?
 - Integrity check fails!
- Lock up memory controller
- System halts immediately (no exploit, but DoS!)

Will SGX save us?

[Userspace] Voltage: -261 mV

[Enclave] Multiplying 0xdeadbeef 0x1122334455667788

[Enclave] Multiply_it. result: 0.





- Public Key Crypto
- Encrypt/Sign messages
- Untrusted channel
- Encrypt/Verify messages with public key
- Decrypt/Sign messages with private key

$$n = p \times q$$

$$S = H^d \bmod n$$

RSA Signature with Chinese Remainder Theorem

$$n = p \times q$$

$$S = H^d \bmod n$$

$$d_p = d \bmod p - 1$$

$$d_q = d \bmod q - 1$$

$$s_1 = H^{d_p} \bmod p$$

$$s_2 = H^{d_q} \bmod q$$

$$n = p \times q$$

$$S = ((q^{-1} \bmod p)(s_1 - s_2) \bmod p) \times q + s_2$$

$$d_p = d \bmod p - 1$$

$$d_q = d \bmod q - 1$$

$$s_1 = H^{d_p} \bmod p$$

$$s_2 = H^{d_q} \bmod q$$

RSA Signature with Chinese Remainder Theorem

$$n = p \times q$$

$$S = ((q^{-1} \bmod p)(s_1 - s_2) \bmod p) \times q + s_2$$

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$$d_q = d \bmod q - 1$$

$$s_1 = H^{d_p} \bmod p$$

$$s_2 = H^{d_q} \bmod q$$

$$n = p \times q$$

$$S = ((q^{-1} \bmod p)(s_1 - s_2) \bmod p) \times q + s_2$$

$$S' = ((q^{-1} \bmod p)(s_1 - s_2) \bmod p) \times q + s_2$$

RSA Signature with Chinese Remainder Theorem

$$n = p \times q$$

$$S = \left(\text{something} \right) \times q + s_2$$

$$S' = \left(\text{something else} \right) \times q + s_2$$

RSA Signature with Chinese Remainder Theorem

$$n = p \times q$$

$$S = \left(\text{something} \right) \times q + s_2$$

$$S' = \left(\text{something else} \right) \times q + s_2$$

$$S - S' = \text{something} \times q$$

RSA Signature with Chinese Remainder Theorem

$$n = p \times q$$

$$S = \left(\overbrace{\text{something}}^{\text{padding}} \right) \times q + s_2$$

$$S' = \left(\overbrace{\text{something else}}^{\text{padding}} \right) \times q + s_2$$

$$S - S' = \text{something} \times q$$

$$q = \gcd(S - S', n)$$

```
uint8_t rsa_dec_ecall(int iterations)
{
    // Wait for first fault
    trigger_fault(iterations);

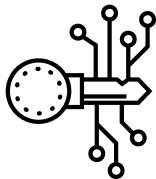
    // Actual decryption
    ippsRSA_Decrypt(ct, dec, pPrv, scratchBuffer);
}
```

```
bagger> dog Enclave/encl
```

MUFARSA

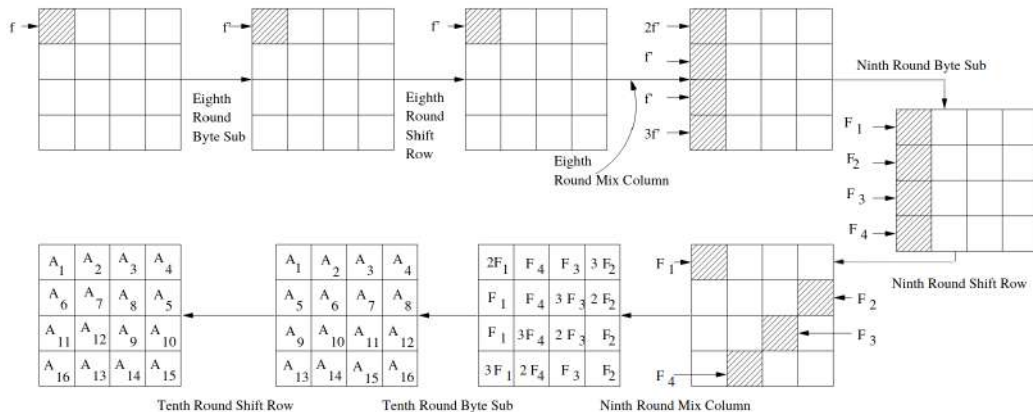
**My Undervolting
Fault Attack on RSA**

**WHAT ELSE
CAN WE
BREAK?!**



- Symmetric Key Crypto
- Encrypt messages for transfer over public channel
- Encrypt data for (untrusted) storage
- 4×4 byte state
- 10 rounds: S-Box, ShiftRows, MixColumns, AddRoundKey

Differential Fault Attack on AES



Michael Tunstall et al. "Differential Fault Analysis of the Advanced Encryption Standard Using a Single Fault". In: *International Workshop on Information Security Theory and Practices*. 2011

AES-NI (New Instructions)

Instruction	Description
AESENC	Perform one round of an AES encryption flow
AESENCLAST	Perform the last round of an AES encryption flow
AESDEC	Perform one round of an AES decryption flow
AESDECLAST	Perform the last round of an AES decryption flow
AESKEYGENASSIST	Assist in AES round key generation
AESIMC	Assist in AES Inverse Mix Columns
PCLMULQDQ	Carryless multiply (CLMUL)

```
do
{
    i++;
    plaintext = <randomly generated>

    result1 = aes128_enc(plaintext);
    result2 = aes128_enc(plaintext);
} while (vec_equal_128(result1,result2) && i<iterations);
```

```
bagger> sudo ./aes-encrypt 100000 -262
```



I have an idea for a logo!



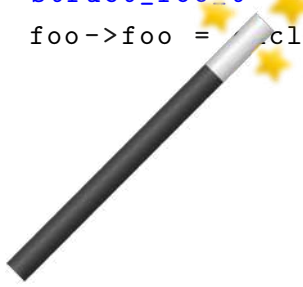
**THIS IS MORE
THAN JUST
CRYPTO**

But what about memory corruption?

```
struct_foo_t *foo = &arr[offset];  
foo->foo = enclave_secret;
```

But what about memory corruption?

```
struct_foo_t *foo = &arr[offset];  
foo->foo = &clave_secret;
```



But what about memory corruption?

```
foo = arr + offset * 0x24
```

But what about memory corruption?

```
foo = arr + offset * 0x24
```



Creating enclave...

==== Victim Enclave ====

[pt.c] /dev/sgx-step opened!

Enclave Base: 0x7f001a000000



Enclave Limit: 0x7f001c000000



EDBGRD: debug

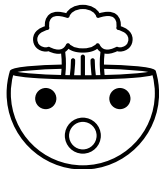


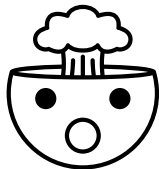
Voltage

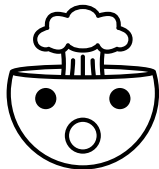
0.584V

Undervolting

-235mV

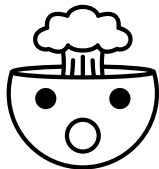






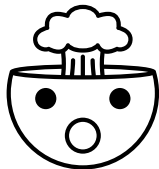
- RSA keys

More than just crypto?



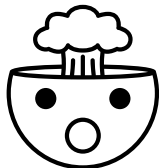
- RSA keys
- AES keys

More than just crypto?



- RSA keys
- AES keys
- Memory corruption

More than just crypto?



- RSA keys
- AES keys
- Memory corruption

→ CryptoVaultScewHammer is not the ideal name!?



- Should be related to undervolting



- Should be related to undervolting
- From protected TEE vaults



- Should be related to undervolting
- From protected TEE vaults
- Steal



- Should be related to undervolting
- From protected TEE vaults
- Steal, corrupt



- Should be related to undervolting
- From protected TEE vaults
- Steal, corrupt, plunder, ...





Bob McGingerbread

@j77tbw



Replying to [@shuckle](#)

Plundervolt is such a great name

12:10 AM · Dec 11, 2019 · [Twitter for iPhone](#)



Trey Forgety @cincvolflt · Dec 11

"**Plundervolt**" is the best **vulnonym** of 2019, and we had to wait until mid-December to get it.



betam4x / Ars Scholae Palatinae

POPULAR

DEC 10, 2019 10:45 PM



Plundervolt? One of the better named exploits of the decade.

↑ **+42** (+42 / 0) ↓

1042 posts | registered 12/23/2013



Jeffrey Joslin

@jeffemera



just pondering the genius behind the name "Plundervolt"



Ravi @ravikumark815 · 6h

Funky **name** for a funky exploit :D

#networksecurity #exploit #cybersecuirty #reverseengineering
#technology



smistephen / Ars Praetorian / *et Subscriptor*

DEC 10, 2019 11:24 PM



I'm wondering if it took longer to actually create Plundervolt, or come up with that double-pun of a name, haha.

↑ **+3** (+3 / 0) ↓

457 posts | registered 7/31/2014



Daniel Cuthbert  @dcuthbert · Dec 11

It might be me but I feel most vulnerability **names** today sound like bad 1980s porno titles

Plundervolt

KNOB attack

Spectre





Victor Fernando R. Ocampo @VictorOcampo · Dec 11

Plundervolt (sounds like a supervillain **name**) can steal all your chips' secure data -





Alex Ionescu @aionescu · Dec 11



I have it on good authority that **PlunderVolt** is the **name** of the villain in Incredibles 3. Missed opportunity for "The Underrrrvolter!" (Shamelessly stolen from [@bfosterjr](#))



Michael Kohl @citizen428 · 16h

I love how new exploits nowadays get cool **names**, landing pages etc. Anyway, **Plundervolt** is pretty interesting. plundervolt.com



↑ [mindovermiles262](#) 19 points · 1 day ago

↓ Can we take a moment and appreciate the quality of those videos? Wow 🤔

↑ [ObscureCulturalMeme](#) 12 points · 1 day ago

↓ Shit, even the website design is easy to read, and on mobile at that. Professional across the board.



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- Breaks the integrity of SGX



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- Within SGX we:



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- A new type of attack
- Breaks the integrity of SGX
- Within SGX we:
 - Retrieve keys from AES-NI
 - Retrieve RSA signature key
 - Induce memory corruption in bug free code
 - Make enclave write secrets to untrusted memory

Kit Murdock, David Oswald, Flavio D. Garcia, Jo Van Bulck, Daniel Gruss, and Frank Piessens. “Plundervolt: Software-based Fault Injection Attacks against Intel SGX”. In: *S&P*. 2020

Plundervolt

Flipping Bits from Software
without Rowhammer

Kit Murdock, Daniel Gruss

December 27, 2019





Susan @SusanMaret · Dec 16

#Plundervolt "is a slightly ponderous pun on Thunderbolt...& the new vulnerability that has its own domain/website, HTTPS certificate, pirate-themed logo, & media-friendly strapline." **#cybersecurity**

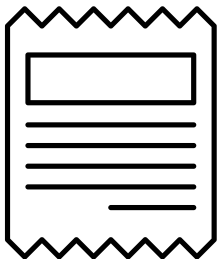
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