

#### Microarchitectural Attacks: From the Basics to Arbitrary Read and Write Primitives without any Software Bugs

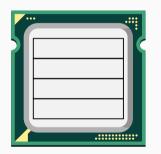
**Daniel Gruss** 

April 11, 2018

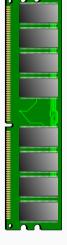
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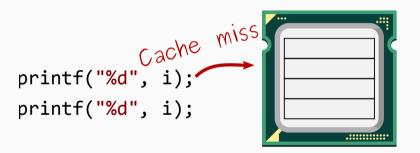
- Daniel Gruss
- Post-Doc @ Graz University of Technology
- Twitter: @lavados
- Email: daniel.gruss@iaik.tugraz.at



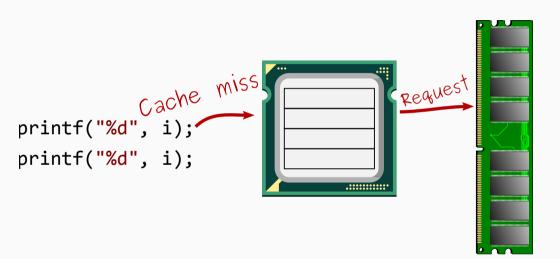


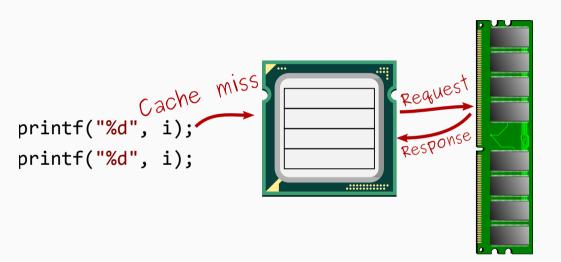
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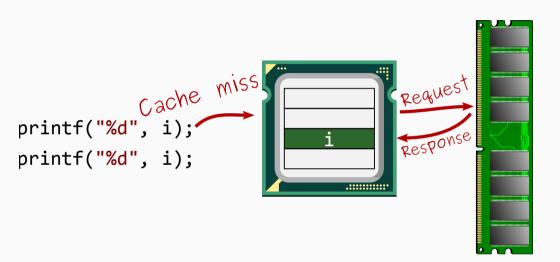


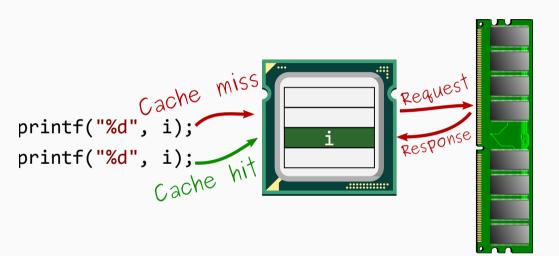




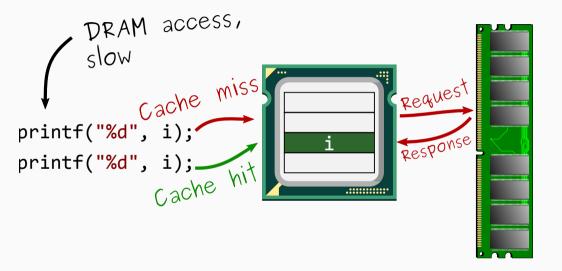




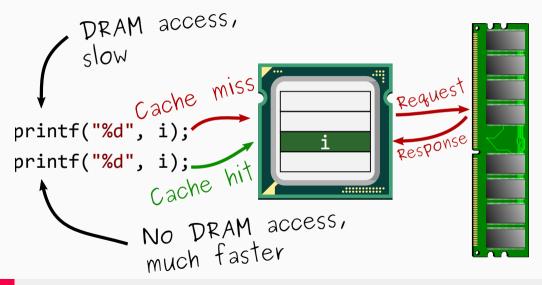


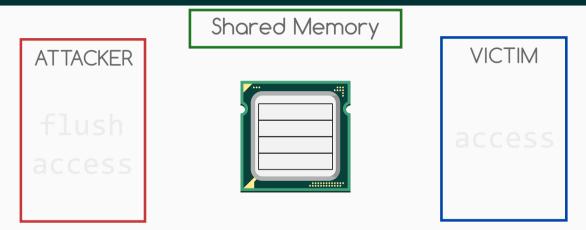


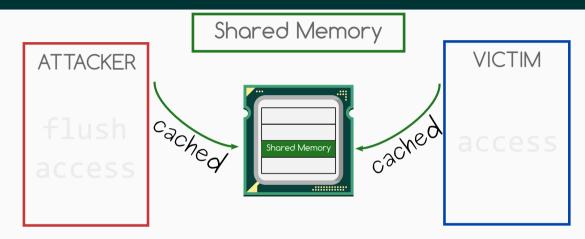
**CPU** Cache

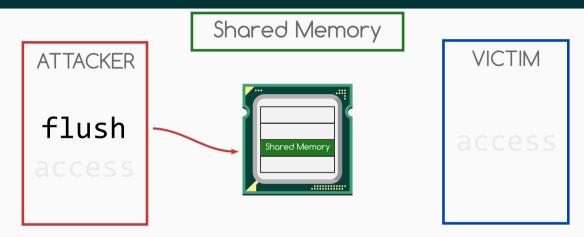


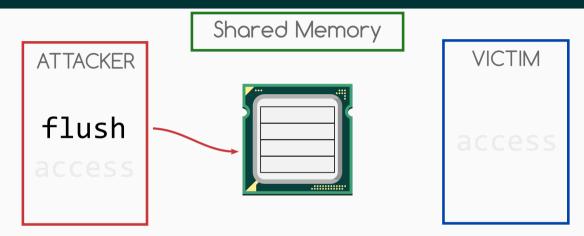
**CPU** Cache

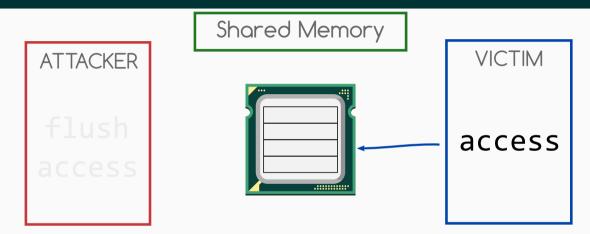


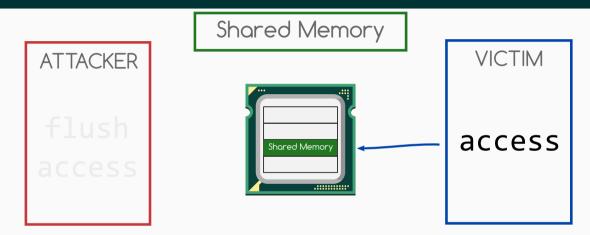


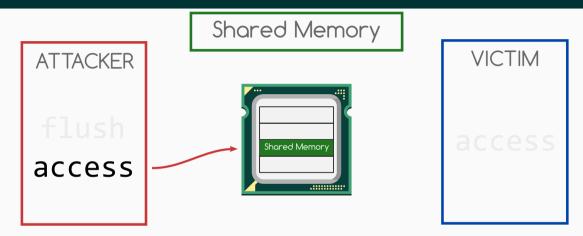


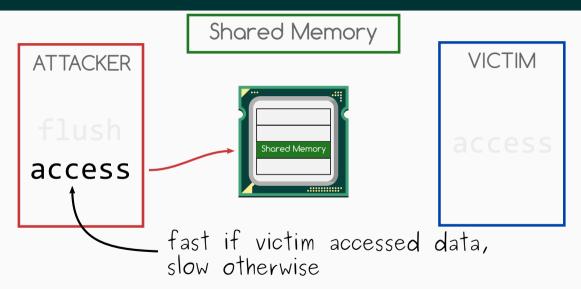


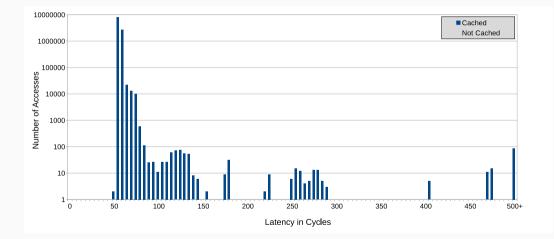


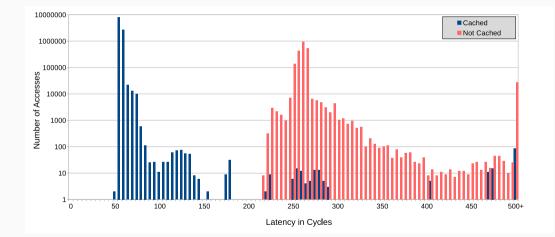










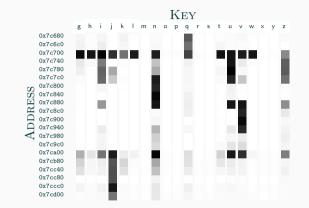


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Cache Template Attack Demo

#### **Cache Template**



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8



7. Serve with cooked and peeled potatoes





## Wait for an hour



## Wait for an hour

# LATENCY

1. Wash and cut vegetables

2. Pick the basil leaves and set aside

3. Heat 2 tablespoons of oil in a pan

4. Fry vegetables until golden and softened



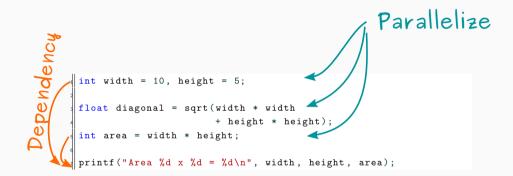
## 1. Wash and cut vegetables

### Parallelize

2. Pick the basil leaves and set aside

3. Heat 2 tablespoons of oil in a pan

4. Fry vegetables until golden and softened







segfault at ffffffff81a000e0 ip 000000000400535
sp 00007ffce4a80610 error 5 in reader



segfault at ffffffff81a000e0 ip 000000000400535 sp 00007ffce4a80610 error 5 in reader

• Kernel addresses are not accessible



segfault at ffffffff81a000e0 ip 000000000400535 sp 00007ffce4a80610 error 5 in reader

- Kernel addresses are not accessible
- Are privilege checks also done when executing instructions out of order?

• Adapted code



```
*(volatile char*)0;
array[84 * 4096] = 0; // unreachable
```

• Adapted code

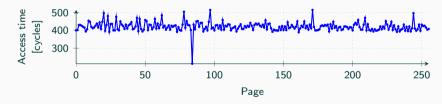


```
*(volatile char*)0;
array[84 * 4096] = 0; // unreachable
```

• Static code analyzer is not happy

1 warning: Dereference of null pointer
2 \*(volatile char\*)0;

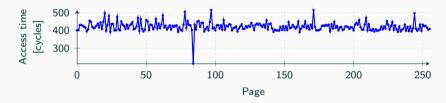




• "Unreachable" code line was actually executed



 $\bullet~\mbox{Flush+Reload}$  over all pages of the array



- "Unreachable" code line was actually executed
- Exception was only thrown afterwards





• Maybe there is no permission check in transient instructions...



- Maybe there is no permission check in transient instructions...
- ...or it is only done when commiting them



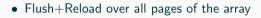
- Maybe there is no permission check in transient instructions...
- ...or it is only done when commiting them
- Indirection through microarchitectural traces:

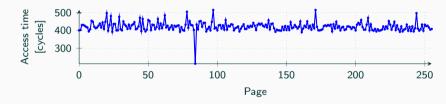


- Maybe there is no permission check in transient instructions...
- ...or it is only done when commiting them
- Indirection through microarchitectural traces:

• Check whether any part of array is cached



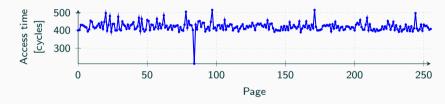




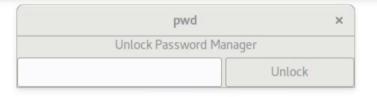
• Index of cache hit reveals data



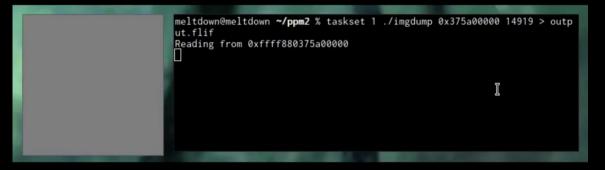
 $\bullet~\mbox{Flush}{+}\mbox{Reload}$  over all pages of the array



- Index of cache hit reveals data
- Permission check is in some cases not fast enough



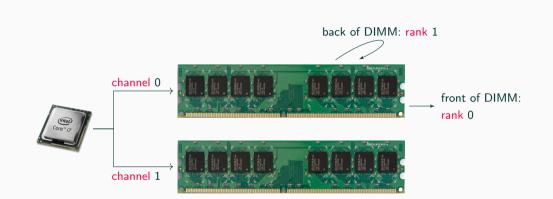
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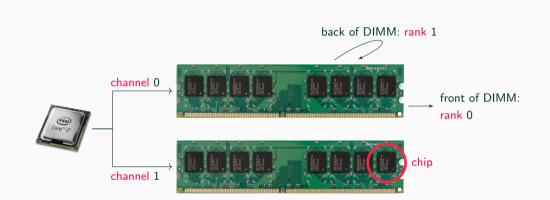


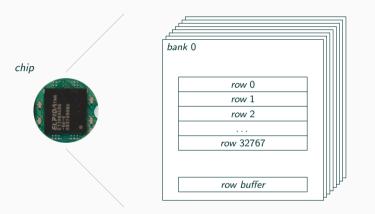
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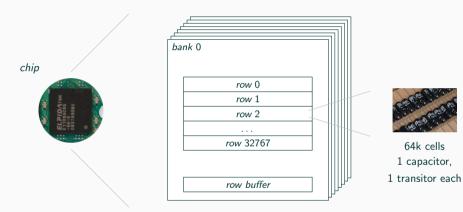


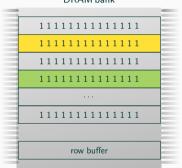




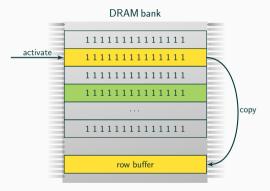




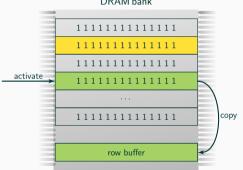




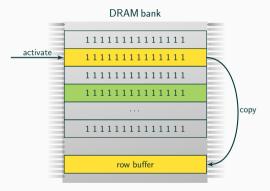
- Cells leak  $\rightarrow$  repetitive refresh necessary
- Maximum interval between refreshes to guarantee data integrity
- Cells leak faster upon proximate accesses  $\rightarrow$  Rowhammer



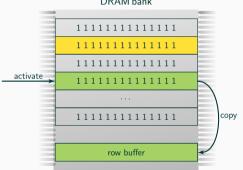
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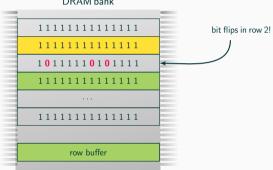
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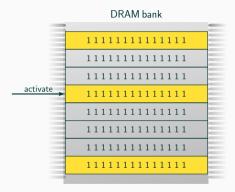
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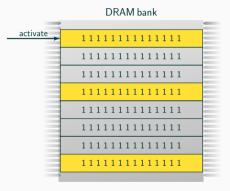


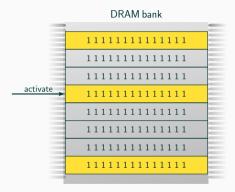
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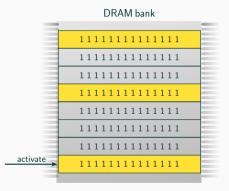


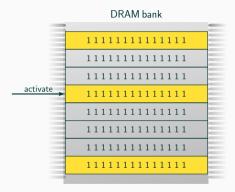
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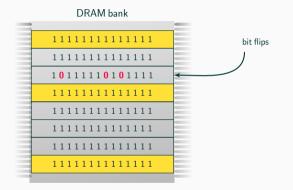


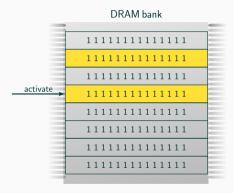


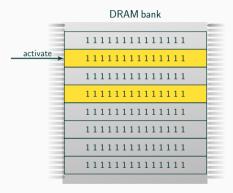


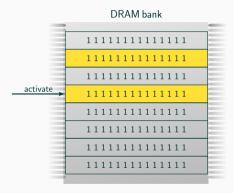


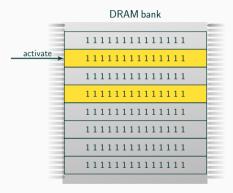


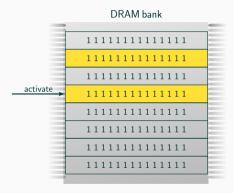


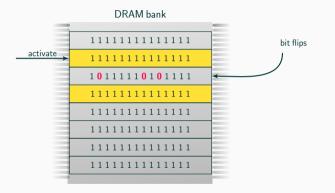


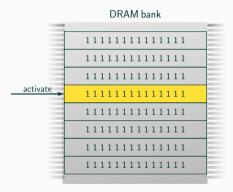






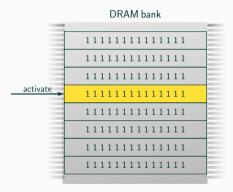






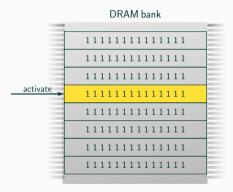
# DRAM bank \_

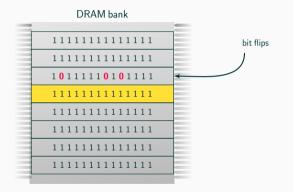
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# DRAM bank \_

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- $\bullet~$  They are not random  $\rightarrow~$  highly reproducible flip pattern!
  - 1. Choose a (kernel) data structure that you can place at arbitrary memory locations
  - 2. Scan for "good" flips
  - 3. Place (kernel) data structure there
  - 4. Trigger bit flip again

• Many applications perform actions as root

- Many applications perform actions as root
- They can be used by unprivileged users as well

- Many applications perform actions as root
- They can be used by unprivileged users as well
- sudo





















• attacks on crypto



 $\bullet$  attacks on crypto  $\rightarrow$  "software should be fixed"



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- attacks on ASLR



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- $\bullet$  attacks on SGX and TrustZone  $\rightarrow$  "not part of the threat model"
- $\bullet$  Rowhammer attacks  $\rightarrow$  "only affects cheap sub-standard modules"
- $\rightarrow\,$  for years we solely optimized for performance



After learning about a side channel you realize:



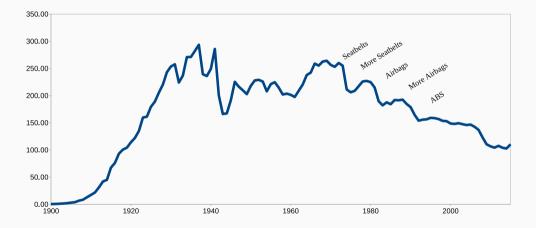
After learning about a side channel you realize:

• the side channels were documented in the Intel manual



After learning about a side channel you realize:

- the side channels were documented in the Intel manual
- only now we understand the implications



Motor Vehicle Deaths in U.S. by Year





• moral obligation to invest more time on defenses than on attacks



- moral obligation to invest more time on defenses than on attacks
- dangerous: we overlooked Meltdown and Spectre for decades



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- we don't know all problems. do we know at least the most important subset?



- moral obligation to invest more time on defenses than on attacks
- dangerous: we overlooked Meltdown and Spectre for decades
- we don't know all problems. do we know at least the most important subset?
- are we hammering on a small subset of problems and forgot about the bigger picture?



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• rethink processor design



- rethink processor design
- grow up, like other fields (car industry, construction industry)



- rethink processor design
- grow up, like other fields (car industry, construction industry)
- find good trade-offs between security and performance

<b>√</b> —	
<b>✓</b> —	

- rethink processor design
- grow up, like other fields (car industry, construction industry)
- find good trade-offs between security and performance
- dedicate more time into identifying problems and not solely in mitigating known problems



# Microarchitectural Attacks: From the Basics to Arbitrary Read and Write Primitives without any Software Bugs

**Daniel Gruss** 

April 11, 2018

Graz University of Technology